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## Atmel START to MPLAB Harmony 3 Migration Guide

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### Introduction

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This document describes the different steps to consider when porting an embedded application from Atmel® START to the MPLAB® Harmony 3 framework. It covers the following migration aspects:

- How to create a MPLAB Harmony 3 project under MPLAB X IDE and navigate through the MPLAB Harmony Configurator (MHC).
- How to identify different elements that compose a project under Atmel START.
- How to port the system/driver/middleware configuration using MPLAB Harmony Configurator.
- Important aspects to consider when porting the project application layer.

To illustrate these aspects, this document references the *SAM D21 IO1 Xplained demo* example available on Atmel START. Prior starting the migration process, ensure that the following prerequisites are met:

- Internet connection with access to <https://start.atmel.com>
- Latest version of MPLAB X IDE <https://www.microchip.com/mplab/mplab-x-ide>
- Latest version of MPLAB Harmony 3 <https://github.com/Microchip-MPLAB-Harmony>
- SAM D21 Xplained Pro board (optional)
- I/O1 Xplained Pro board (optional)

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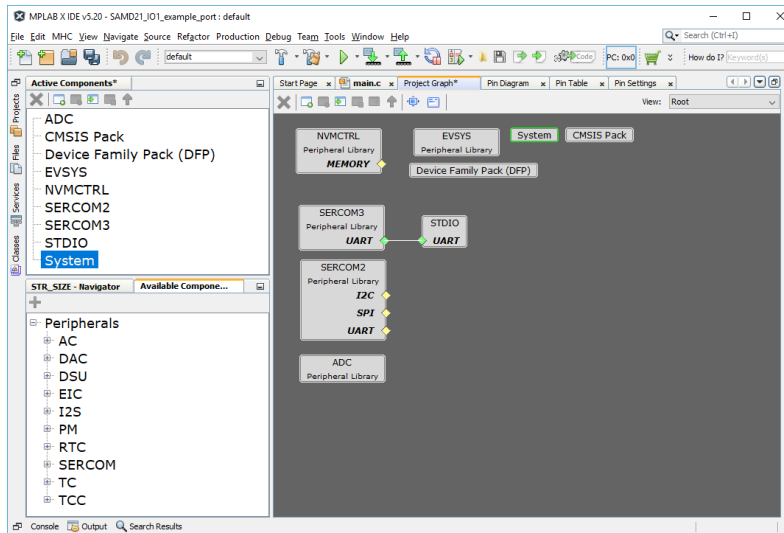
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### 1. Creating MPLAB Harmony 3 Project Under MPLAB X IDE

The first step in this process is creating a MPLAB Harmony 3 project under MPLAB X IDE. This new project will enable access to the MPLAB Harmony 3 Configurator (MHC), and access the list of software modules, such as peripheral libraries, services and middleware, which are available for the target SAM device.

Figure 1-1. MPLAB Harmony 3 Configurator Available Components

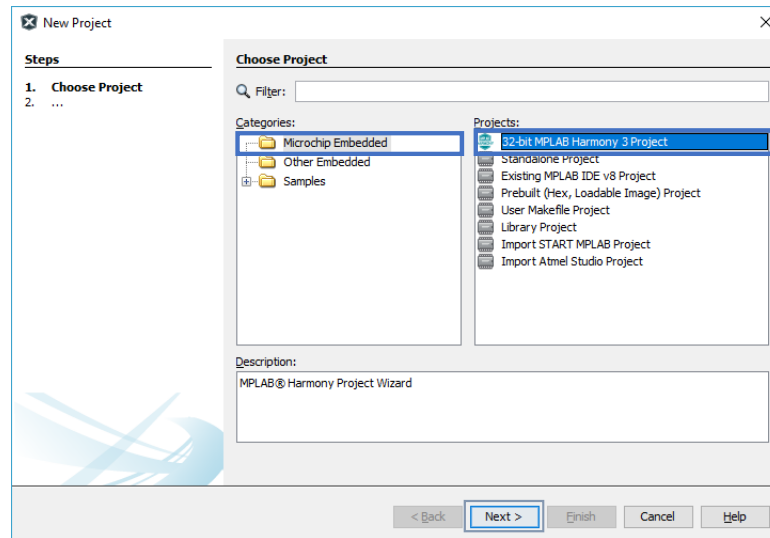


To create a project for the SAM D21J18A device, follow these steps:

1. Launch MPLAB X IDE.
2. In MPLAB X IDE, select *File > New Project* (or click the New Project icon).
3. In the New Project window, under Steps, select Choose Project, and then under Choose Project section, select these options: for Categories select Microchip Embedded, and for Projects select 32-bit MPLAB Harmony 3 Project.

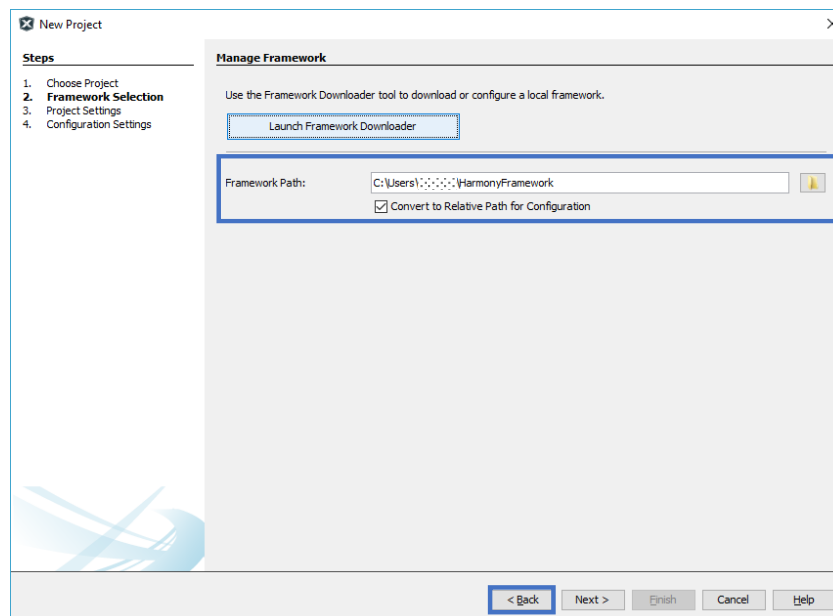
**Note:** If the option 32-Bit MPLAB Harmony 3 Project is not available, users need to install the MPLAB Harmony 3 Configurator plug-in by selecting *Tools > Plugins > Available Plugins* before continuing with this demonstration. MPLAB Harmony 3 Configurator overview is available for download at <https://microchipdeveloper.com/harmony3:mhc-overview>.

Figure 1-2. Creating an MPLAB Harmony 3-Based New Project



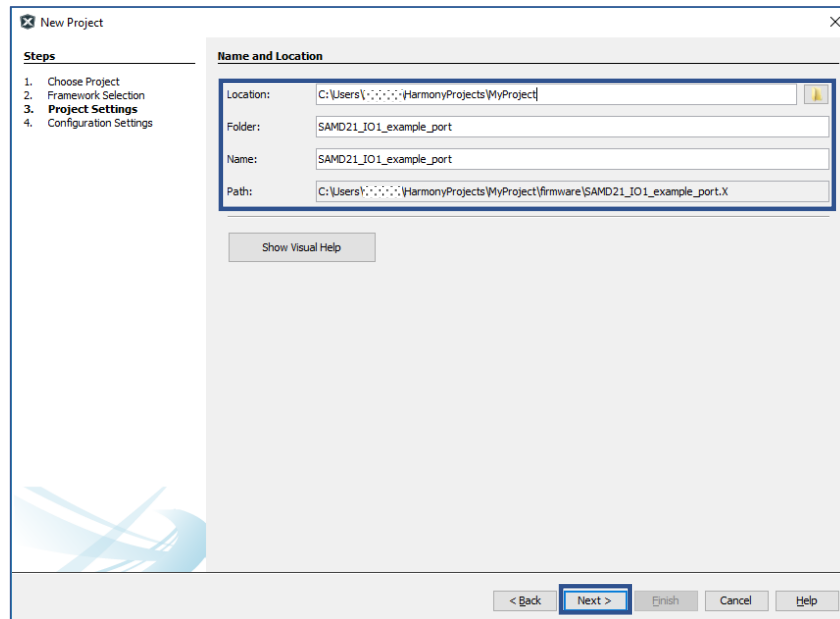
4. Click **Next**.
5. Select Framework Selection, and under Manage Framework section, enter Framework Path (Path to the folder in which the MPLAB Harmony 3 packages are downloaded). For this demonstration, the MPLAB Harmony 3 packages are already downloaded at D:\microchip\github\h3.

Figure 1-3. New Project - Framework Selection



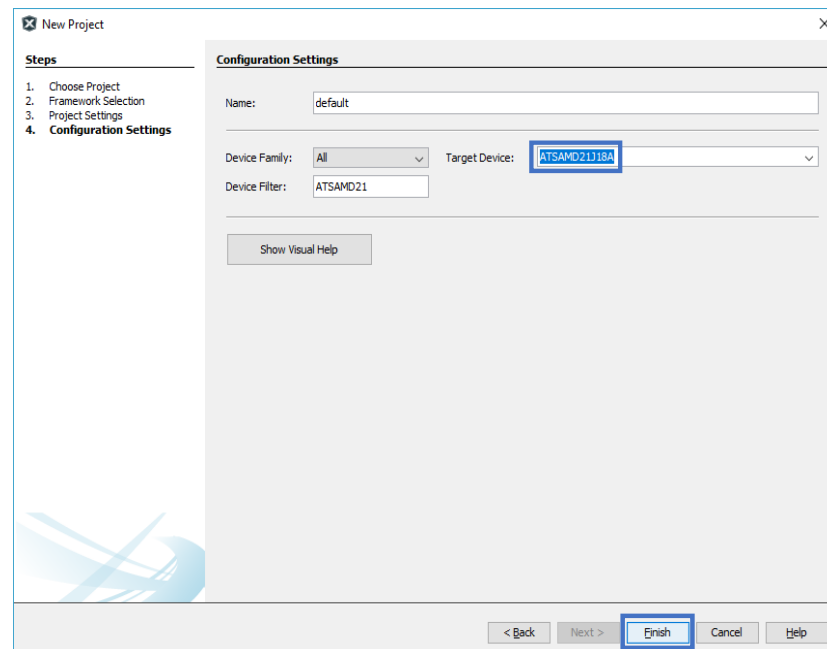
6. Click **Next**.
7. Select Project Settings, and under Name and Location section enter MPLAB Harmony 3 new project details:
  - Location: Indicates the path to the root folder of the new project. All project files will be placed inside this folder.
  - Folder: Indicates the name of the MPLAB X IDE folder.
  - Name: Enter name of the project. This name will be shown in the MPLAB X IDE.
  - Path: It is read-only box. It will update as and when users make changes to other entries.

Figure 1-4. New Project Wizard Step 3



8. Click **Next**.
9. Select Configuration Settings, and then enter details as given below:
  - Name: Enter the configuration name.
  - Target Device: Choose a device name.
10. Click **Finish**.

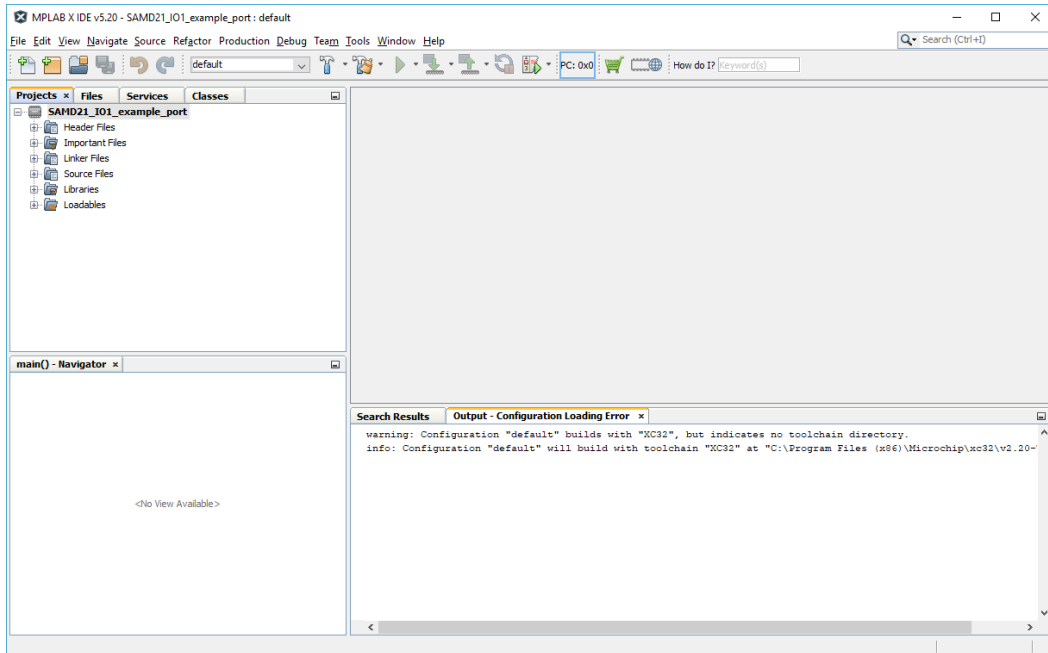
Figure 1-5. New Project Wizard Step 4



The new MPLAB Harmony 3 project will be launched.

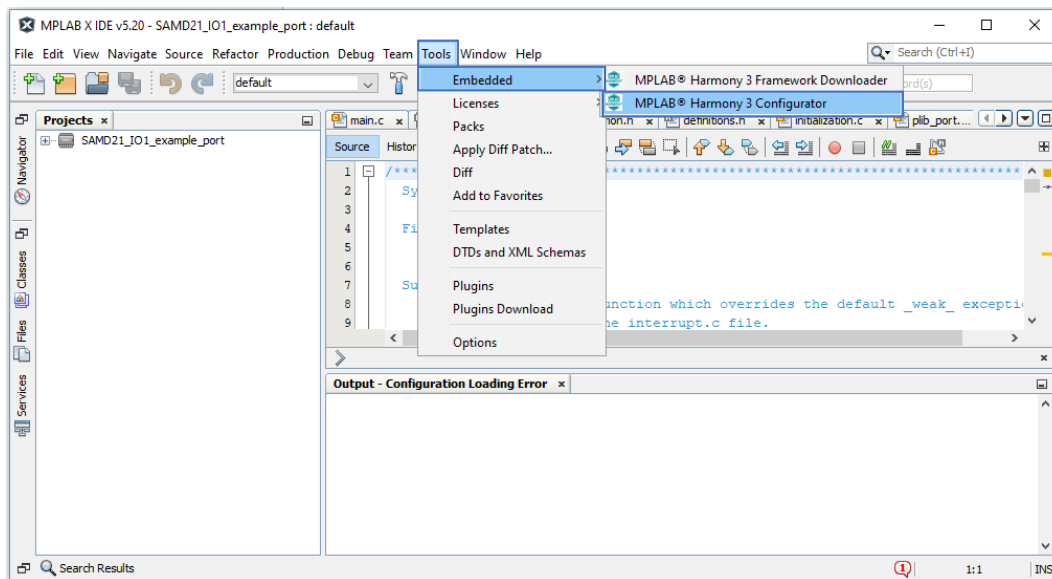
**Note:** After clicking the Finish button, if MHC does not launch, the user can launch it by selecting *Tools > Embedded > MPLAB® Harmony 3 Configurator* from the menu bar of the MPLAB X IDE.

Figure 1-6. New MPLAB Harmony 3 Project



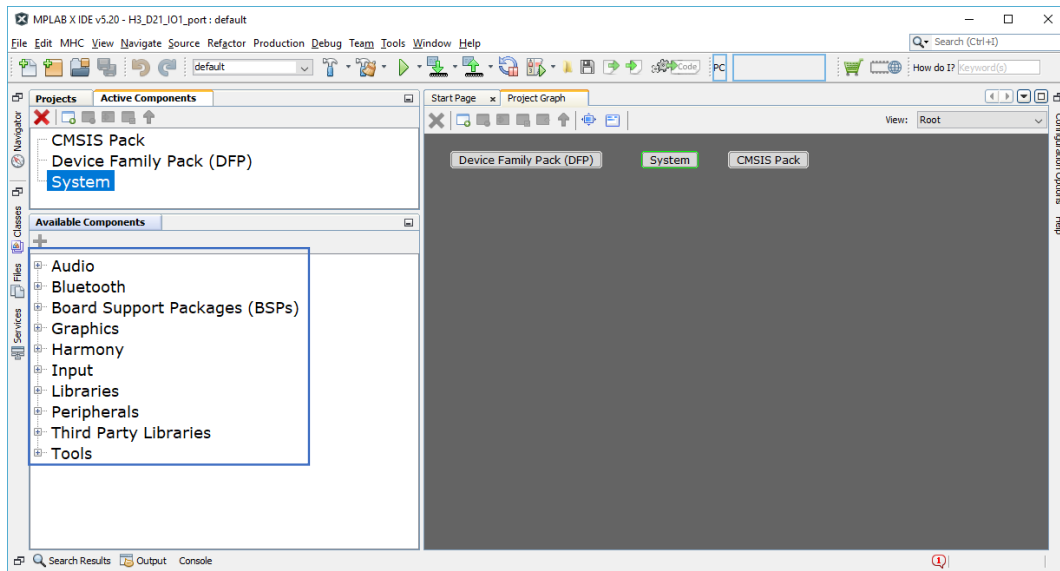
The software modules associated with the project configuration are seen by selecting *Tools > Embedded > MPLAB Harmony 3 Configurator*.

Figure 1-7. Accessing MPLAB Harmony 3 Configurator



The associated MPLAB Harmony 3 Configurator list will be displayed as shown in the figure below.

Figure 1-8. MPLAB Harmony 3 Configurator - Available Component List



### 2. Identifying the Resources Used in the Atmel START Project

Before porting the application, users need to identify the software modules that compose the application. As both Atmel START and MPLAB Harmony 3 frameworks provide a graphical configuration interface, hence it is simple to identify the different drivers and middleware modules that are used by the application and compare them to the available MPLAB Harmony 3 modules.

Figure 2-1. Atmel START Graphical Configuration Interface

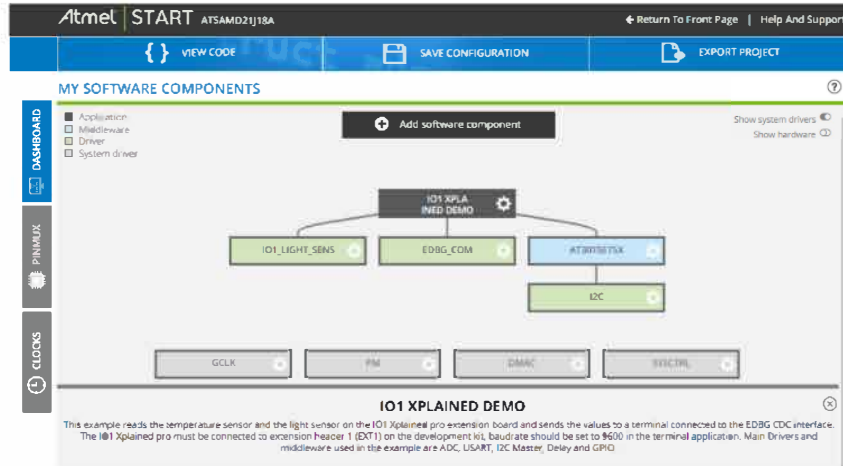
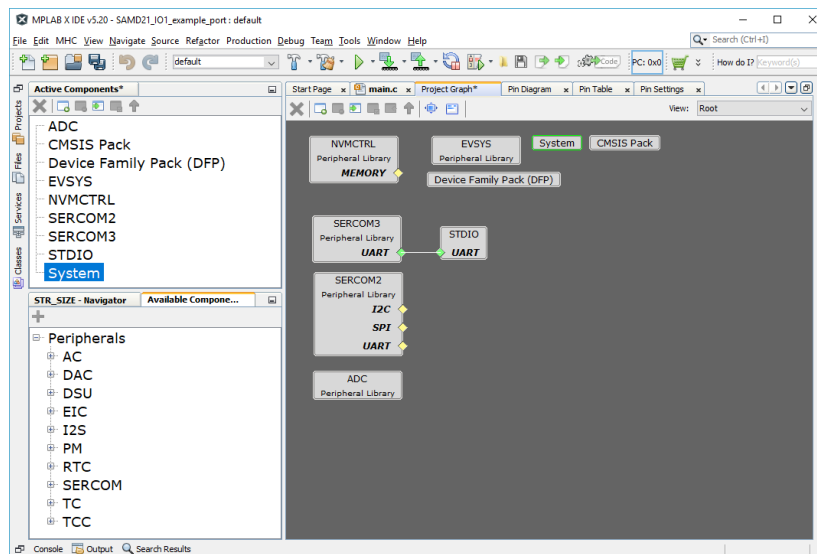


Figure 2-2. Harmony 3 Graphical Configuration Interface (MHC)



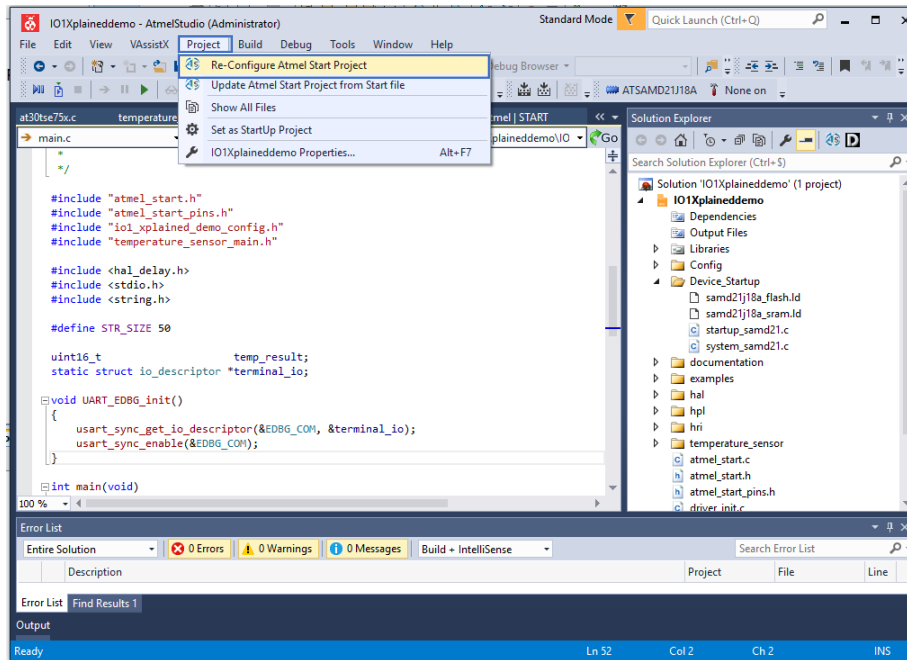
**Note:** Some applications include additional software functions or middleware that are not part of Atmel START. Therefore, it is important to review the full application flowchart or project architecture to identify them.

Follow these steps while using the SAM D21 I/O1 example from Atmel START as a reference.

1. In Atmel Studio 7, open the Atmel START project to be ported by selecting *Project > Re-Configure Atmel Start Project*.



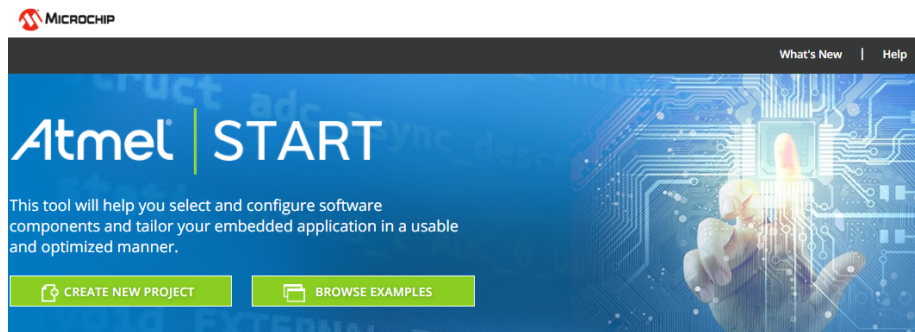
Figure 2-3. Reconfiguring Atmel START Project



**Note:** The project can also be opened by loading its `.atstart.atzip` file at <https://start.atmel.com/>.

Atmel START main window showing Load Project tab is displayed.

Figure 2-4. Load Project From a File Under Atmel START



### Getting started

To get started you can either create a new project from scratch or open an existing example. In both cases you can configure your software components and device settings such as clocks and pin layout. When you are done, you can export your project and open it using your favourite IDE for further development.

For more information on how to use Atmel START, read the [Getting Started guide](#) or watch our [video tutorials](#).



### Load existing project

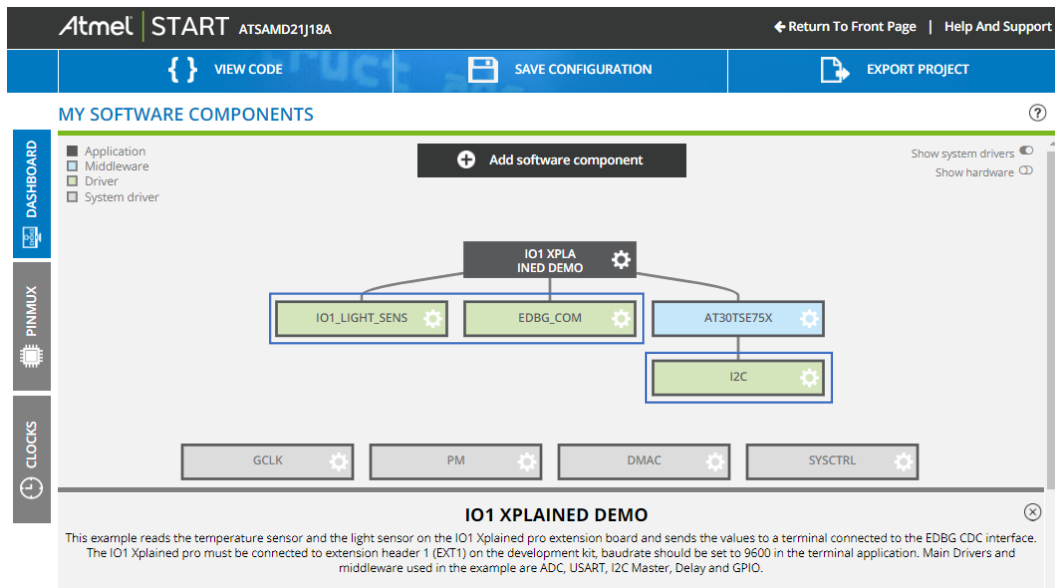


Use this option if you want to restore a locally saved project. Browse and select either a project file (\*.atzip) or a configuration file (\*.atstart, \*.json).

Your latest project will always be stored in your web browser. Use this option to pick up where you left off.

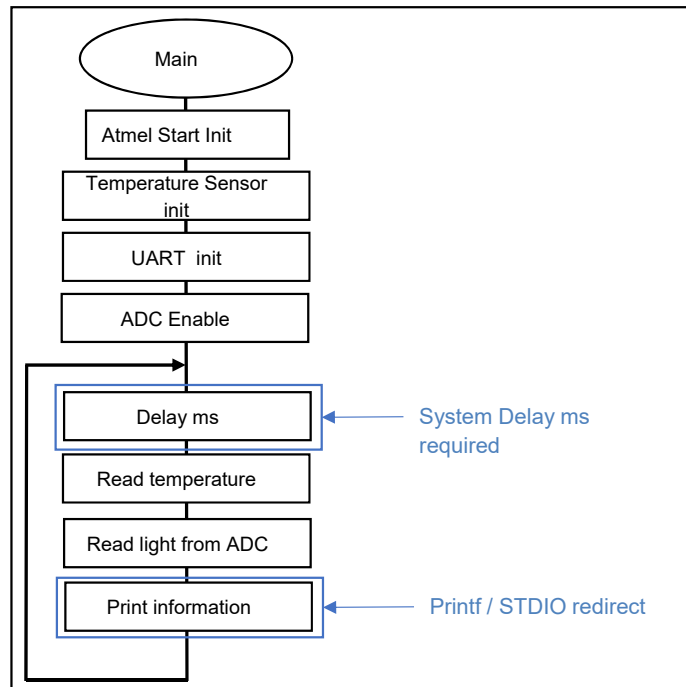
2. When project is loaded, click on the Dashboard tab and identify the Atmel START drivers and its configuration.

**Figure 2-5. Driver Identification Under Atmel START**



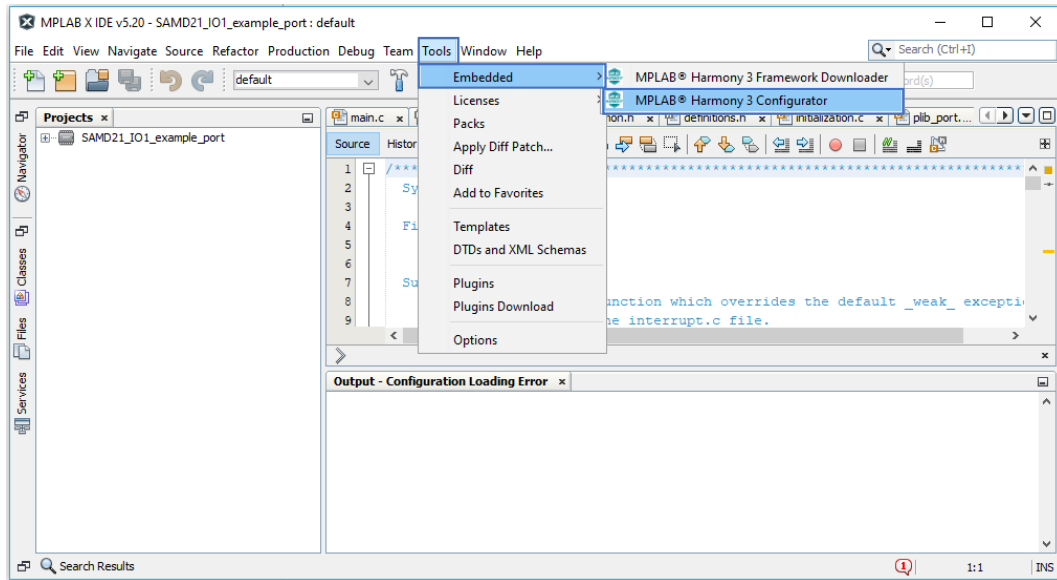
3. Check the application code and application flowchart to identify whether any additional services and middleware are required. The figure below illustrates the SAM D21 I/O1 flowchart.

**Figure 2-6. Additional Service Middleware Identification Using a Flowchart**



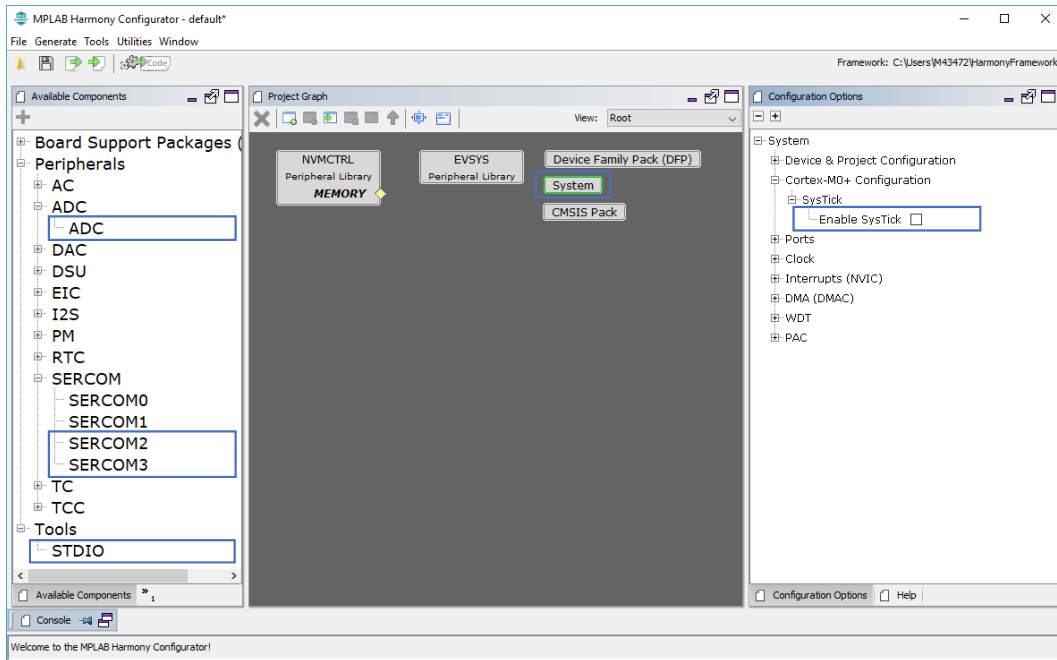
4. If MPLAB Harmony Configurator is not opened, from MPLAB X IDE open the MPLAB Harmony 3 Configurator by selecting *Tools > Embedded > MPLAB Harmony 3 Configurator*.

Figure 2-7. Accessing MPLAB Harmony 3 Configurator



5. Search for software module availability in Harmony 3 Configurator, and then drag and drop it into the Project Graph window.

Figure 2-8. Identifying Software Module in MHC



The table below provides the drivers and middleware used for this demonstration.

**Table 2-1. Drivers and Middleware**

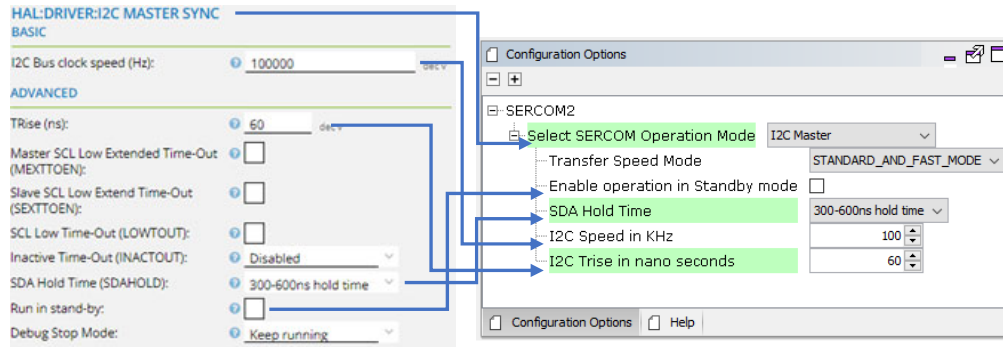
	ATMEL START Module	Harmony 3 Module
Drivers	IO1_LIGHT_SENS	ADC
	I <sup>2</sup> C	SERCOM2
	EDBG_COM	SERCOM3
	Systick (implicit)	SYSTEM > SYSTICK
Middleware	AT30SE75X	None
	STDIO Redirect	STDIO

**Note:** Specific development is required for AT30SE75X. Other drivers, system services, and middleware are available under MPLAB Harmony 3.

### 3. Porting Driver Configuration from Atmel START

To port a driver from Atmel Studio to MPLAB Harmony 3 is a straightforward operation as it relies on the comparison of Atmel START and MPLAB Harmony Configurator Graphical interfaces.

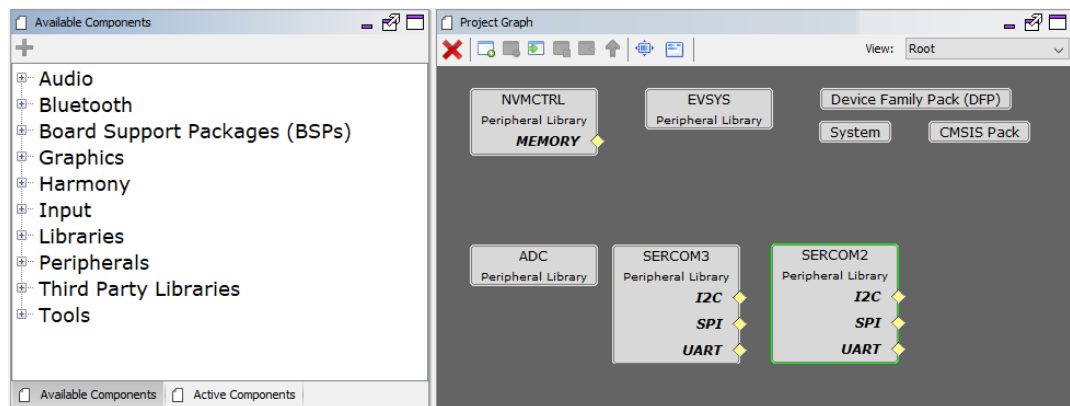
**Figure 3-1. Atmel START to MPLAB X Driver Support**



Follow these steps for the SAM D21 I/O1 Xplained demonstration:

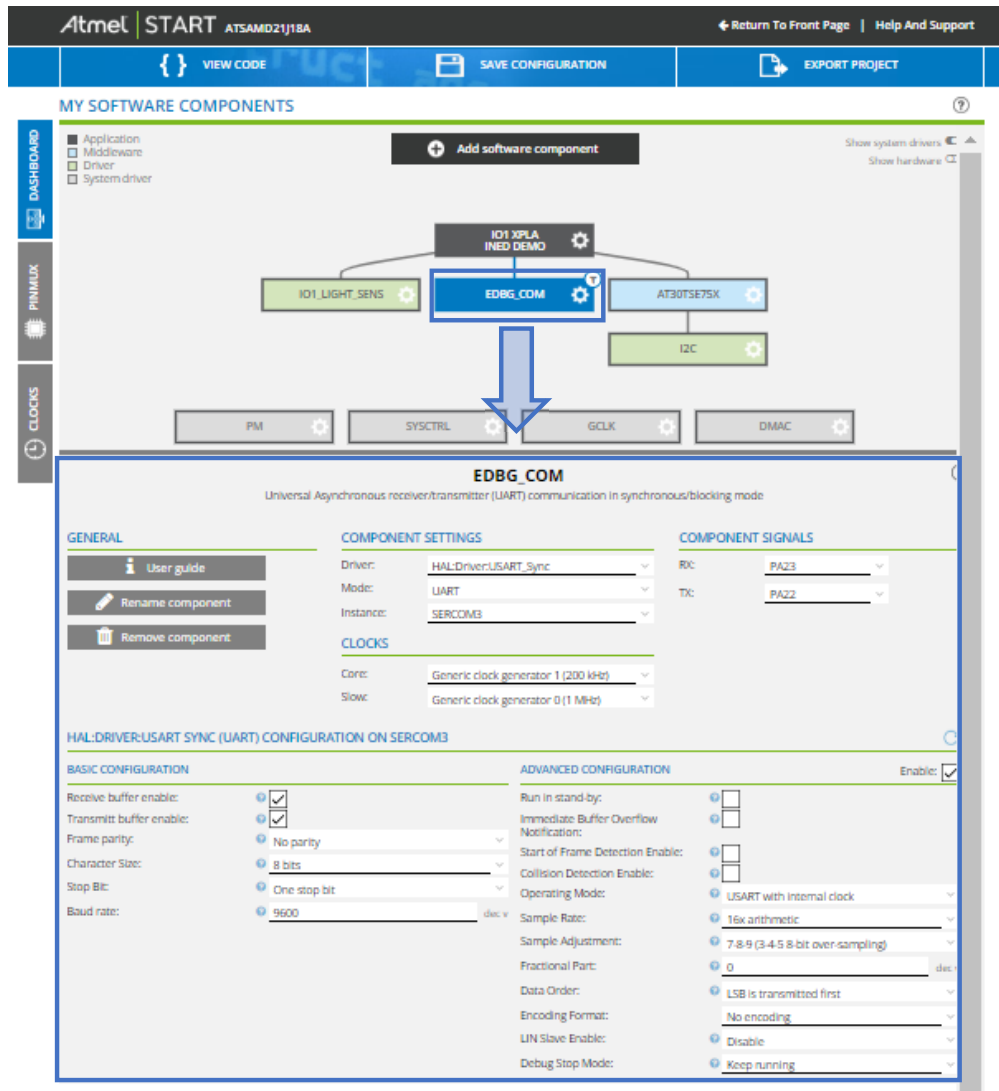
1. Add peripheral library (drivers) to the MPLAB Harmony 3 project by dragging them from the Available Component list to the Project Graph section.

**Figure 3-2. Add Peripheral Driver from the Available Component List**



2. Configure all the MPLAB Harmony 3 peripheral libraries according to their equivalent driver in Atmel START. **Note:** Specific Atmel START driver configurations can be seen by clicking individually on each of the driver component in the application dashboard interface.

Figure 3-3. Access Driver Configuration in Atmel START



- ADC peripheral Library (IO1\_LIGHT\_SENS) configuration.

**Figure 3-4. Port ADC Driver Configuration**

HAL:DRIVER:ADC SYNC (ADC) CONFIGURATION ON ADC

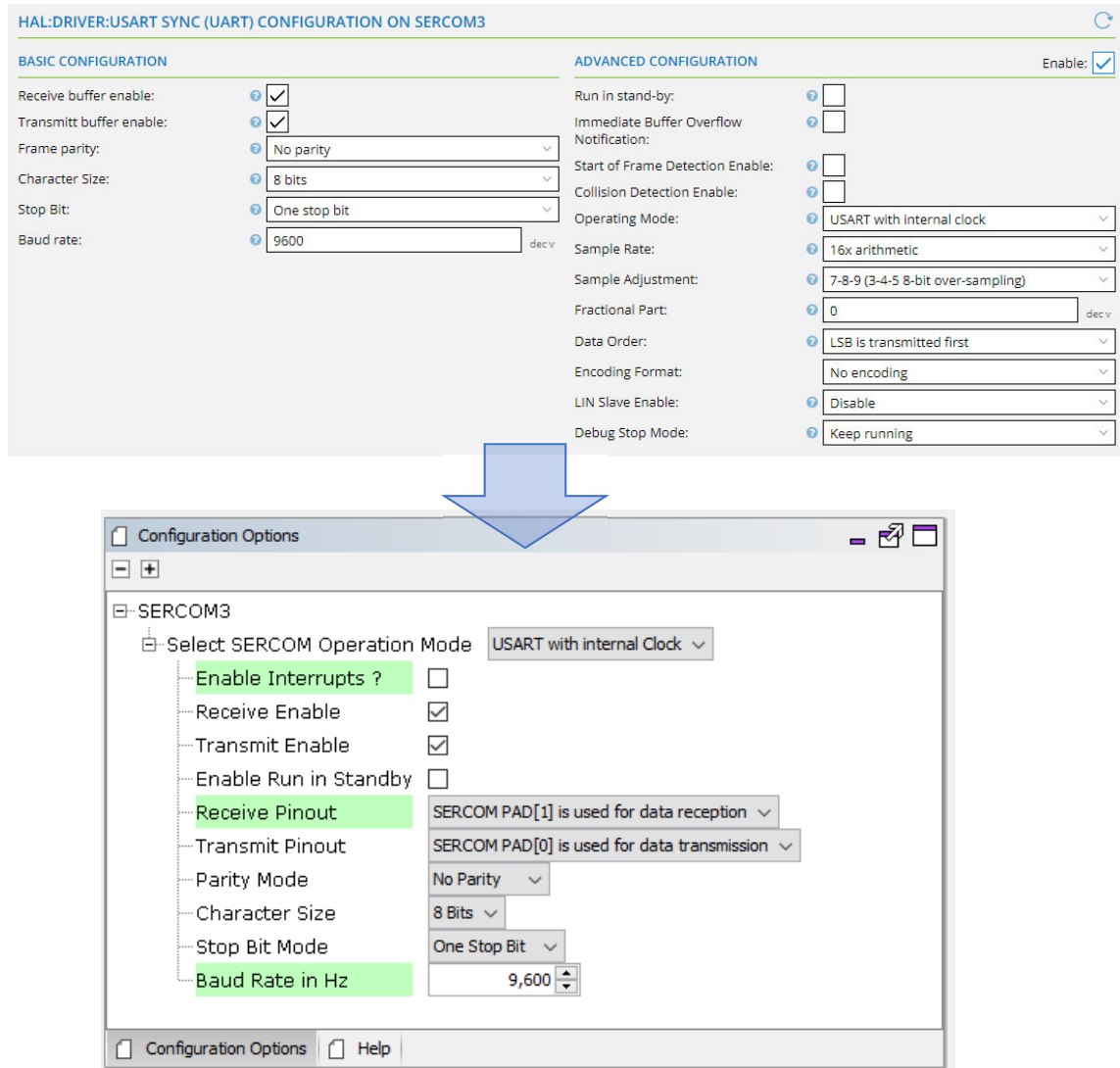
BASIC CONFIGURATION	ADVANCED CONFIGURATION
Conversion resolution: 12-bit	Run in standby: <input type="checkbox"/>
Reference Selection: 1.0V voltage reference	Debug Run: <input type="checkbox"/>
Prescaler configuration: Peripheral clock divided by 4	Left-Adjusted Result: <input type="checkbox"/>
Free Running Mode: <input type="checkbox"/>	Reference Buffer Offset Compensation Enable: <input type="checkbox"/>
Differential Mode: <input type="checkbox"/>	Digital Correction Logic Enabled: <input type="checkbox"/>
Positive Mux Input Selection: ADC AIN8 pin	Offset Correction Value: 0 dec v
Negative Mux Input Selection: Internal ground	Gain Correction Value: 0 dec v
EVENT CONTROL Enable: <input type="checkbox"/>	Gain Factor Selection: 1x
	Adjusting Result / Division Coefficient: 0 dec v
	Number of Samples to be Collected: 1 sample
	Sampling Time Length: 0 dec v
	Window Monitor Mode: No window mode
	Window Monitor Lower Threshold: 0 dec v
	Window Monitor Upper Threshold: 0 dec v
	Number of Input Channels Included in Scan: 0 dec v
	Positive Mux Setting Offset: 0 dec v

Configuration Options

- ADC
  - Select Prescaler: Peripheral clock divided by 4
  - Select Sample Length (cycles): 4
  - \*\*\*\* Conversion Time is 64.0 uS \*\*\*\*
  - Select Gain: 1x
  - Select Reference: 1/2 VDDANA (only for VDDANA > 2.0V)
  - Select Conversion Trigger: SW Trigger
  - Channel Configuration
    - Select Positive Input: ADC AIN8 Pin
    - Select Negative Input: Internal Ground
    - Number of inputs to scan: 0
  - Result Configuration
    - Select Result Resolution: 12-bit result
    - Left Aligned Result:
    - Enable Result Ready Interrupt:
    - Enable Result Ready Event Out:
  - Window Mode Configuration
    - Select Window Monitor Mode: No window mode (default)
  - Sleep Mode Configuration
    - Run During Standby:

– SERCOM3 peripheral Library (EDBG\_COM) configuration

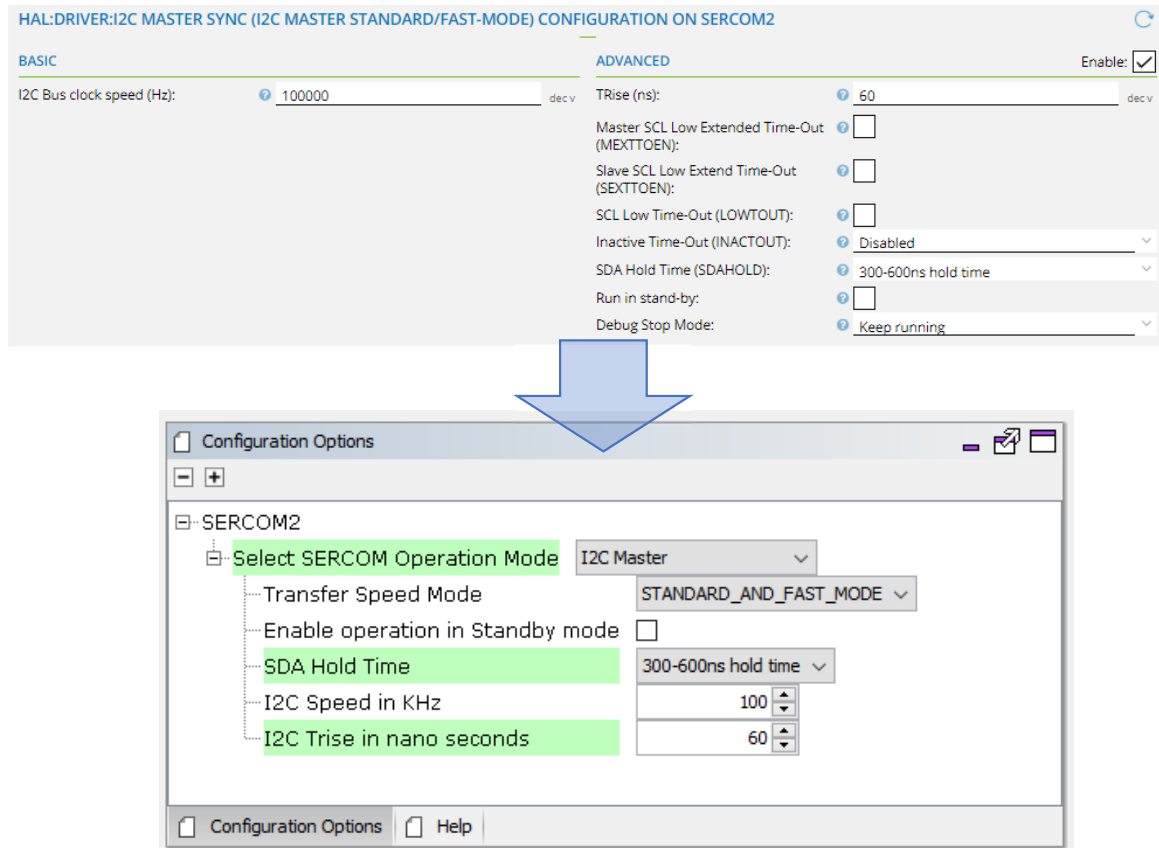
**Figure 3-5. Port SERCOM3 Driver Configuration**



– SERCOM2 peripheral Library (I<sup>2</sup>C) configuration

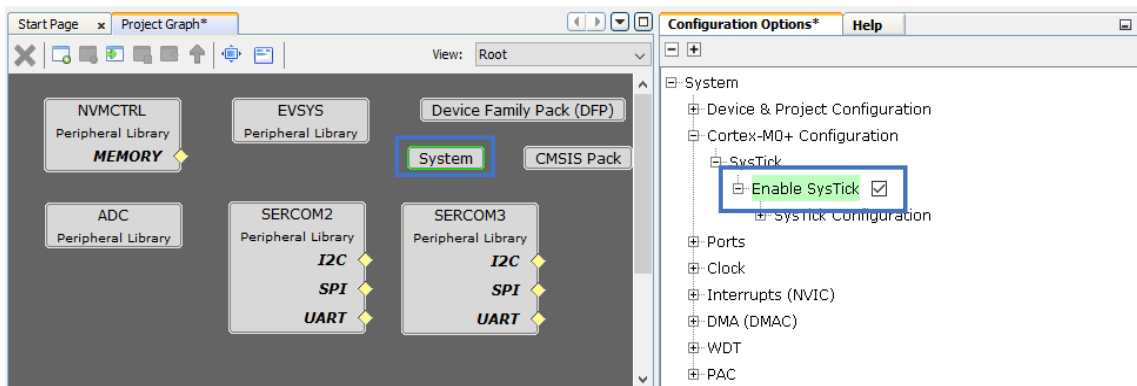


**Figure 3-6. Port SERCOM2 Driver Configuration**



- SYSTICK Service configuration

**Figure 3-7. Enable Service Under MHC**




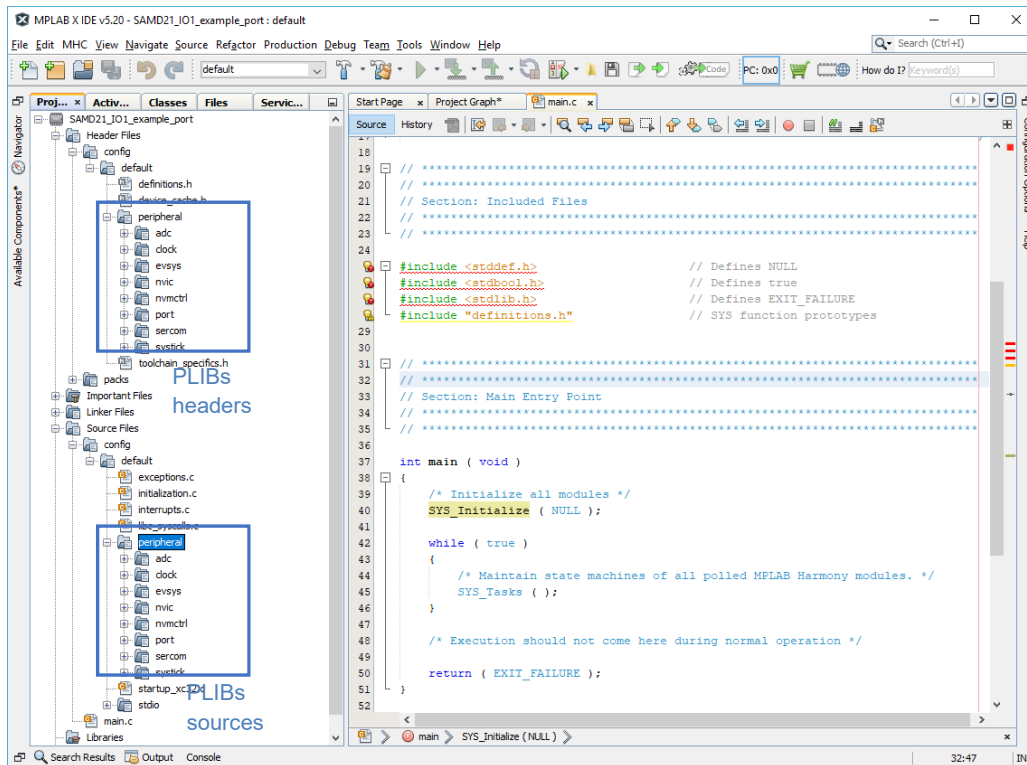
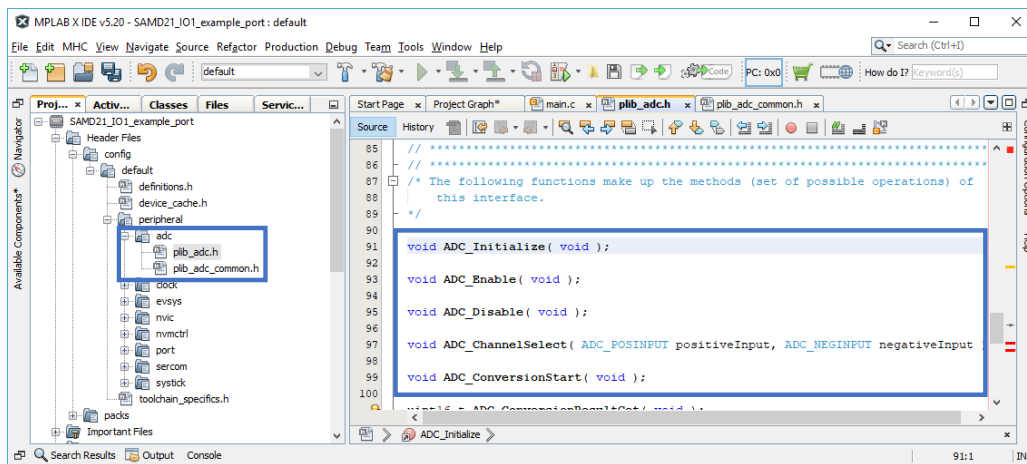
After generating the project source code from the MHC interface by clicking  or selecting *MHC > Generate Code*, the peripheral libraries (PLIBs) configuration can be found in the following project directories:

Figure 3-8. Generated MHC Project Architecture



The API function definition used by the application can be found in the peripheral library header files.

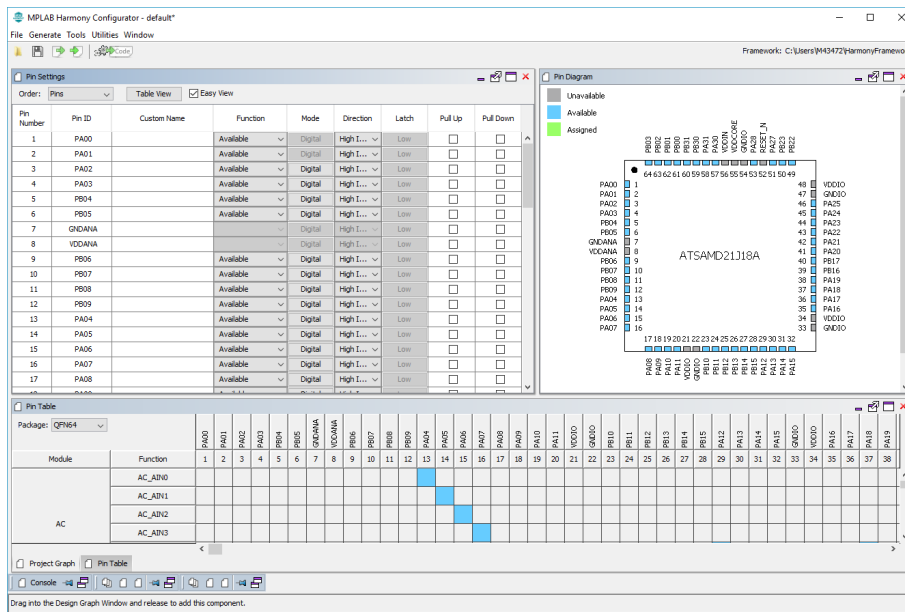
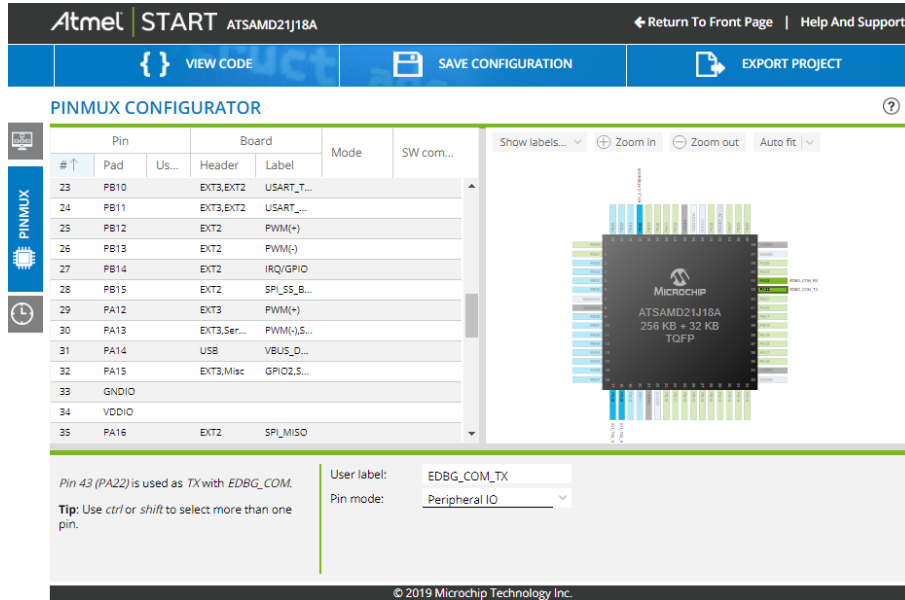
Figure 3-9. Peripheral Libraries API Location



### 4. Porting the System PINMUX Configuration from Atmel START

Porting an application PINMUX configuration from Atmel Studio to MPLAB Harmony 3 is a straightforward operation as it relies on comparison as each framework graphical interface offers similar capabilities.

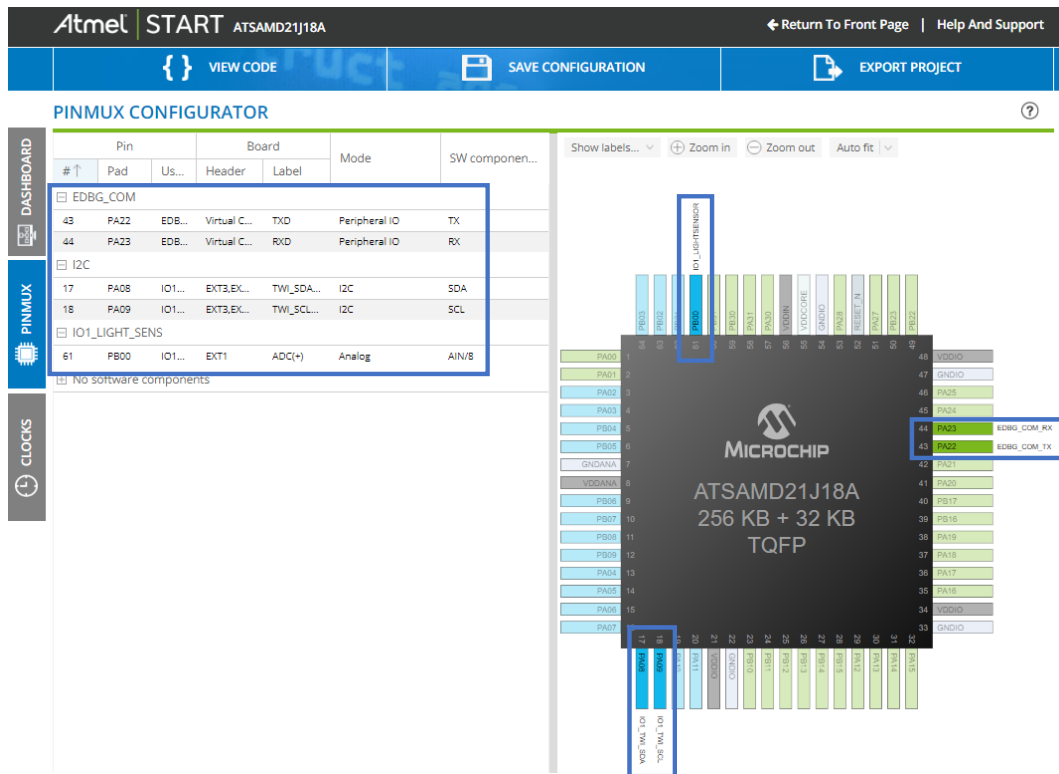
Figure 4-1. Atmel START and MHC Pin Configuration Interface Comparison



Follow these steps for the SAM D21 IO1 Xplained demonstration:

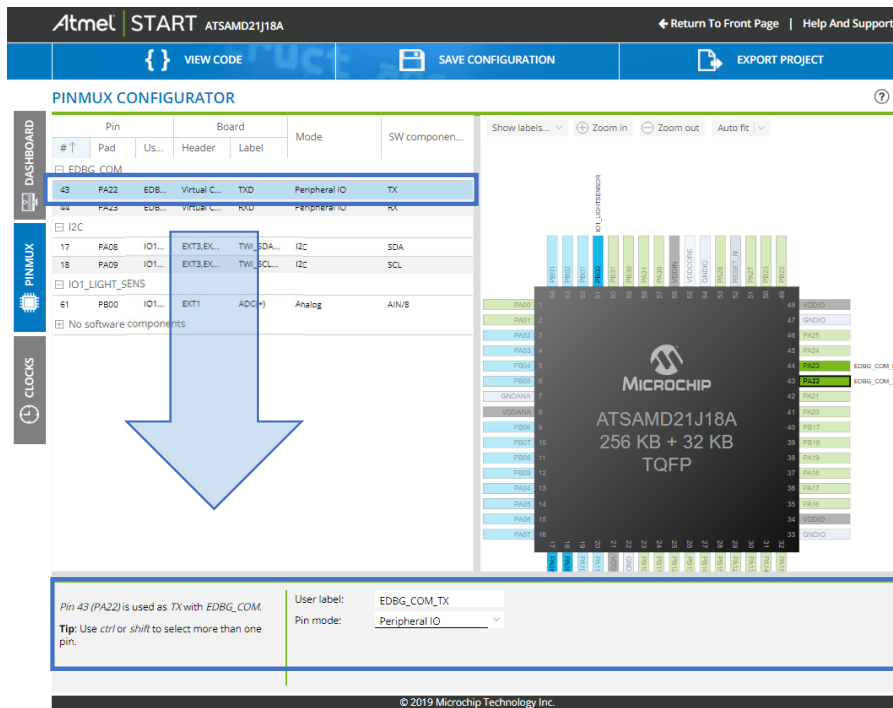
1. In Atmel START, click on the **PINMUX** tab and identify which I/O pins are configured and used in the application.

Figure 4-2. Identify I/O Pins Configuration Under Atmel START



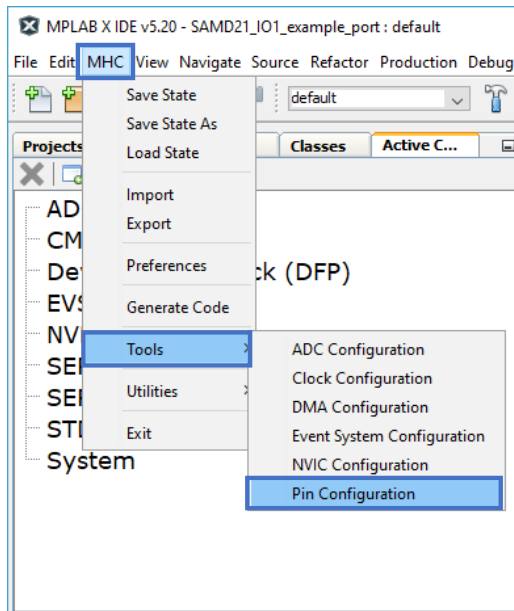
**Note:** The configuration of each pin is available by clicking on it in the PINMUX configurator table.

Figure 4-3. Identify I/O Pins Configuration Under Atmel START (Continued)



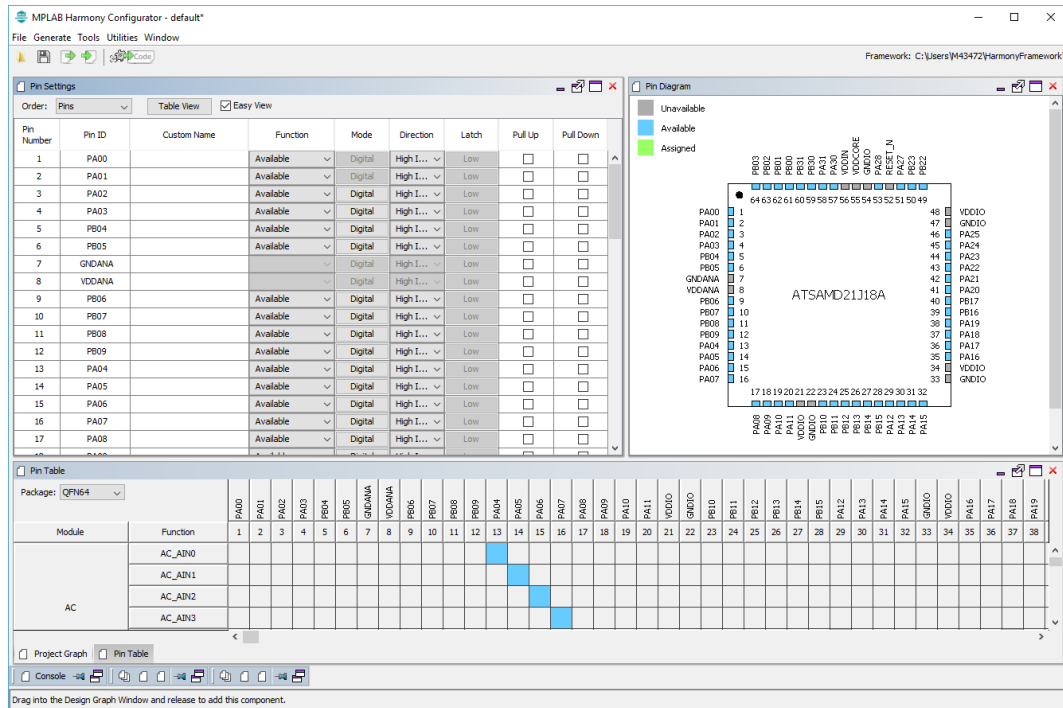
- In MPLAB X IDE, select *MHC > Tools > Pin Configuration* to open the project pin configurator interface.

Figure 4-4. Accessing MHC Under MPLAB X



**Note:** The following tab in the MHC interface enables the customization of the application pin configuration.

Figure 4-5. Accessing Pin Configuration Windows Under the MHC



3. Port the I/O pin configuration from Atmel Studio to the MHC Pin Setting tab.
  - SERCOM 2 I/O settings

**Figure 4-6. SERCOM 2 I/O Settings**

Pin 17 (PA08) is used as SDA with I2C.  
**Tip:** Use *ctrl* or *shift* to select more than one pin.

User label:  Pull configuration:

Pin mode:

Pin 18 (PA09) is used as SCL with I2C.  
**Tip:** Use *ctrl* or *shift* to select more than one pin.

User label:  Pull configuration:

Pin mode:

Start Page x main.c x Project Graph\* x Pin Diagram x Pin Table x Pin Settings x

Order: Pins Table View Easy View

Pin Number	Pin ID	Custom Name	Function	Mode	Direction	Latch	Pull Up	Pull Down
10	PB07		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
11	PB08		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
12	PB09		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
13	PA04		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
14	PA05		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
15	PA06		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
16	PA07		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
17	PA08	SERCOM2_PAD0	SERCOM2_P...	Digital	High ...	n/a	<input type="checkbox"/>	<input type="checkbox"/>
18	PA09	SERCOM2_PAD1	SERCOM2_P...	Digital	High ...	n/a	<input type="checkbox"/>	<input type="checkbox"/>
19	PA10		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
20	PA11		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
21	VDDIO			Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
22	GNDIO			Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
23	PB10		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
24	PB11		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
25	PB12		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>

– SERCOM 3 I/Os settings

**Figure 4-7. SERCOM 3 I/O settings**

Pin 43 (PA22) is used as TX with EDBG\_COM.

**Tip:** Use *ctrl* or *shift* to select more than one pin.

User label:

Pin mode:

Pin 44 (PA23) is used as RX with EDBG\_COM.

**Tip:** Use *ctrl* or *shift* to select more than one pin.

User label:

Pin mode:

Start Page x main.c x Project Graph\* x Pin Diagram x Pin Table x Pin Settings x

Order: Pins v Table View  Easy View

Pin Number	Pin ID	Custom Name	Function	Mode	Direction	Latch	Pull Up	Pull Down
37	PA18		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
38	PA19		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
39	PB16		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
40	PB17		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
41	PA20		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
42	PA21		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
43	PA22	SERCOM3_PAD0	SERCOM3_P...	Digital	High ... v	n/a	<input type="checkbox"/>	<input type="checkbox"/>
44	PA23	SERCOM3_PAD1	SERCOM3_P...	Digital	High ... v	n/a	<input type="checkbox"/>	<input type="checkbox"/>
45	PA24		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
46	PA25		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
47	GNDIO			Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
48	VDDIO			Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
49	PB22		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
50	PB23		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
51	PA27		Available v	Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>
52	RESET_N			Digital	High ... v	Low	<input type="checkbox"/>	<input type="checkbox"/>

– ADC I/Os settings

Figure 4-8. ADC I/O Settings

Pin 61 (PB00) is used as AIN/8 with IO1\_LIGHT\_SENS.

User label:

Pin mode:

Pin Number	Pin ID	Custom Name	Function	Mode	Direction	Latch	Pull Up	Pull Down
49	PB22		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
50	PB23		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
51	PA27		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
52	RESET_N			Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
53	PA28		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
54	GNDIO			Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
55	VDDCORE			Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
56	VDDIN			Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
57	PA30		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
58	PA31		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
59	PB30		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
60	PB31		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
61	PB00	ADC_AIN8	ADC_AIN8	Analog	High ...	n/a	<input type="checkbox"/>	<input type="checkbox"/>
62	PB01		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
63	PB02		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>
64	PB03		Available	Digital	High ...	Low	<input type="checkbox"/>	<input type="checkbox"/>

- Verify that the MHC pin diagram is aligned with the initial Atmel START project pin diagram.

Figure 4-9. Pin Configuration Verification


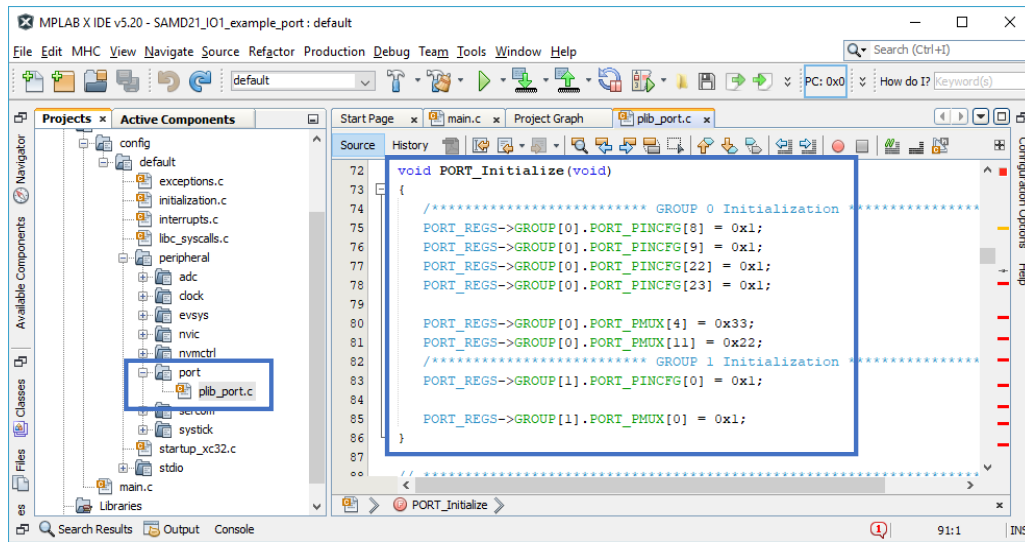
After regenerating the project source from the MHC interface by clicking , the port initialization routine executed during program initialization can be found in the following project files. This initialization routine is automatically generated by the MHC according to the user configuration entered in the graphical interface.



Figure 4-10. MHC Generated Pin Configuration

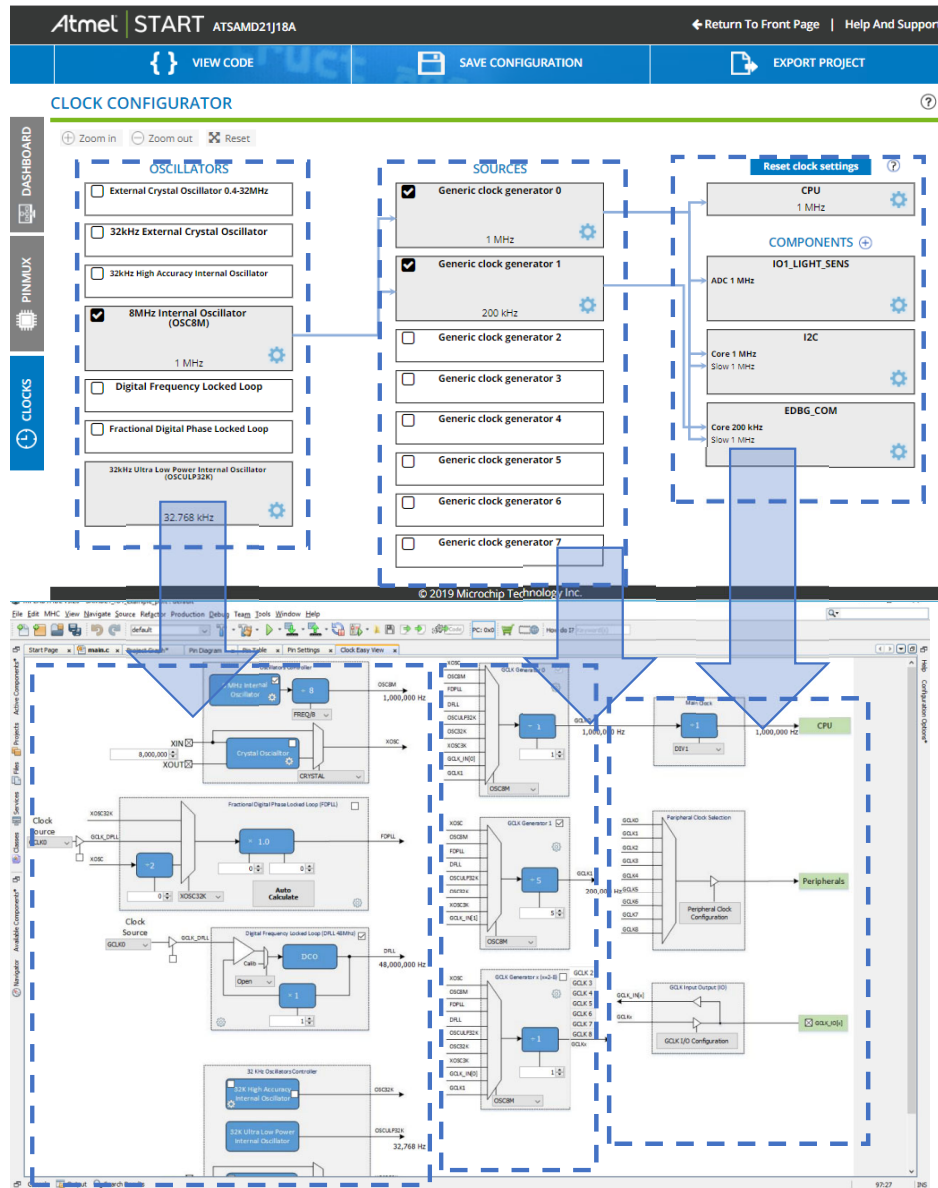


**Note:** During application run-time, the PORT configuration can be changed by calling APIs from the MPLAB Harmony 3 PORT PLIB.

### 5. Porting the System Clock Configuration from Atmel START

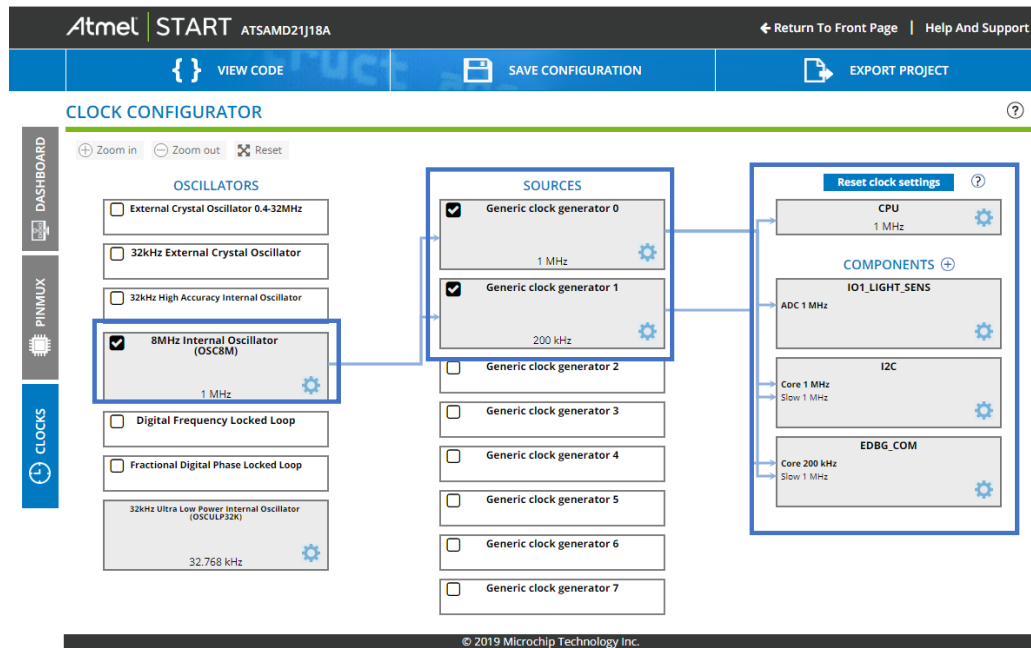
The porting of an application clock configuration from Atmel Studio to MPLAB Harmony 3 is a straightforward operation as it relies on the comparison of both framework graphical interfaces which offer similar capabilities.

Figure 5-1. Atmel START and MHC Clock Configuration Interface Comparison



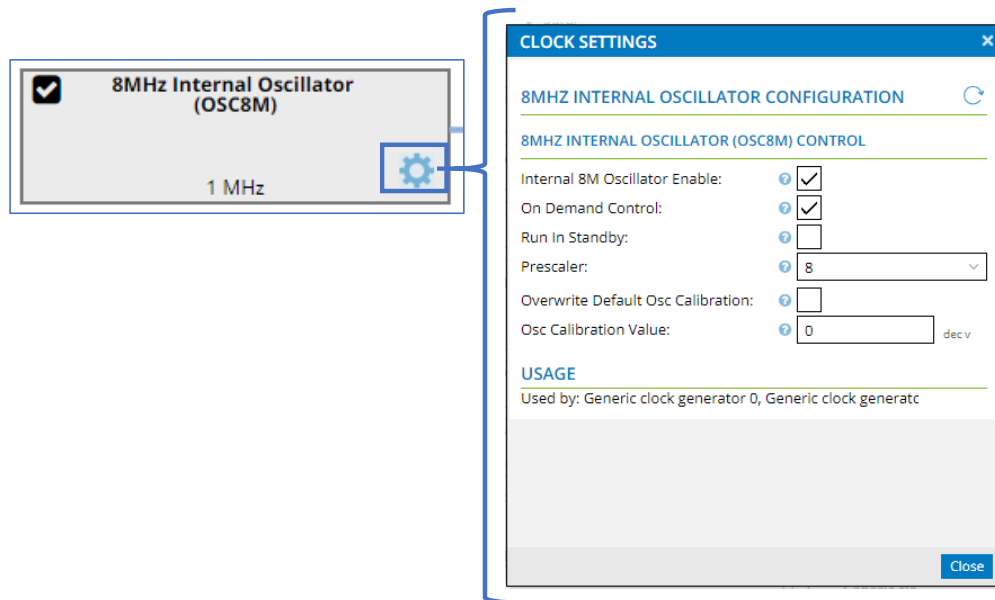
1. In Atmel START, click the **PINMUX** tab and identify different clock elements used in the application and their settings.

Figure 5-2. Identify Project Clock Configuration Under Atmel START



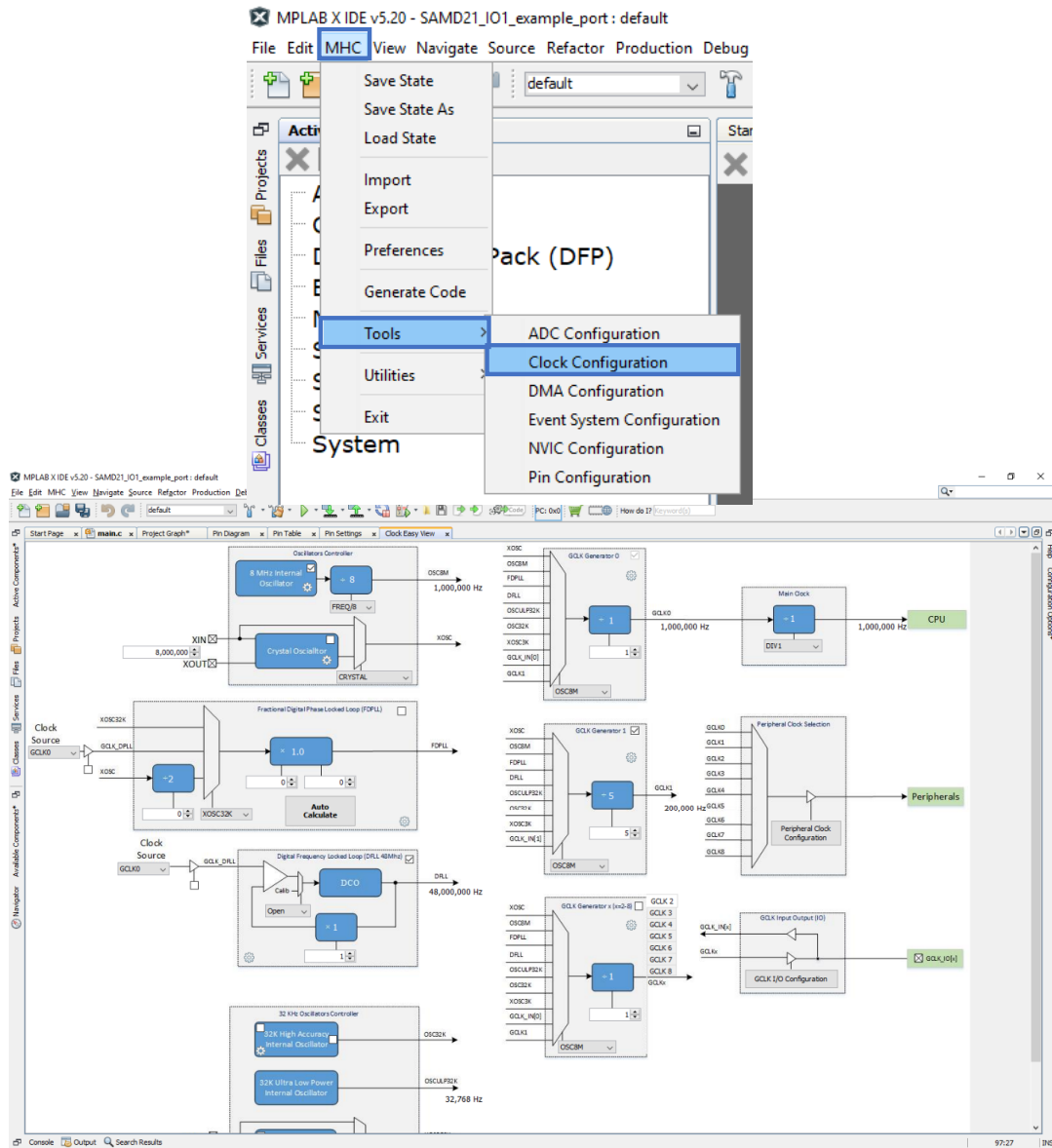
**Note:** Additional information on the settings for a specific clock element can be retrieved by clicking on the associated blue gear wheel button.

Figure 5-3. Access Clock Element Settings Under Atmel START



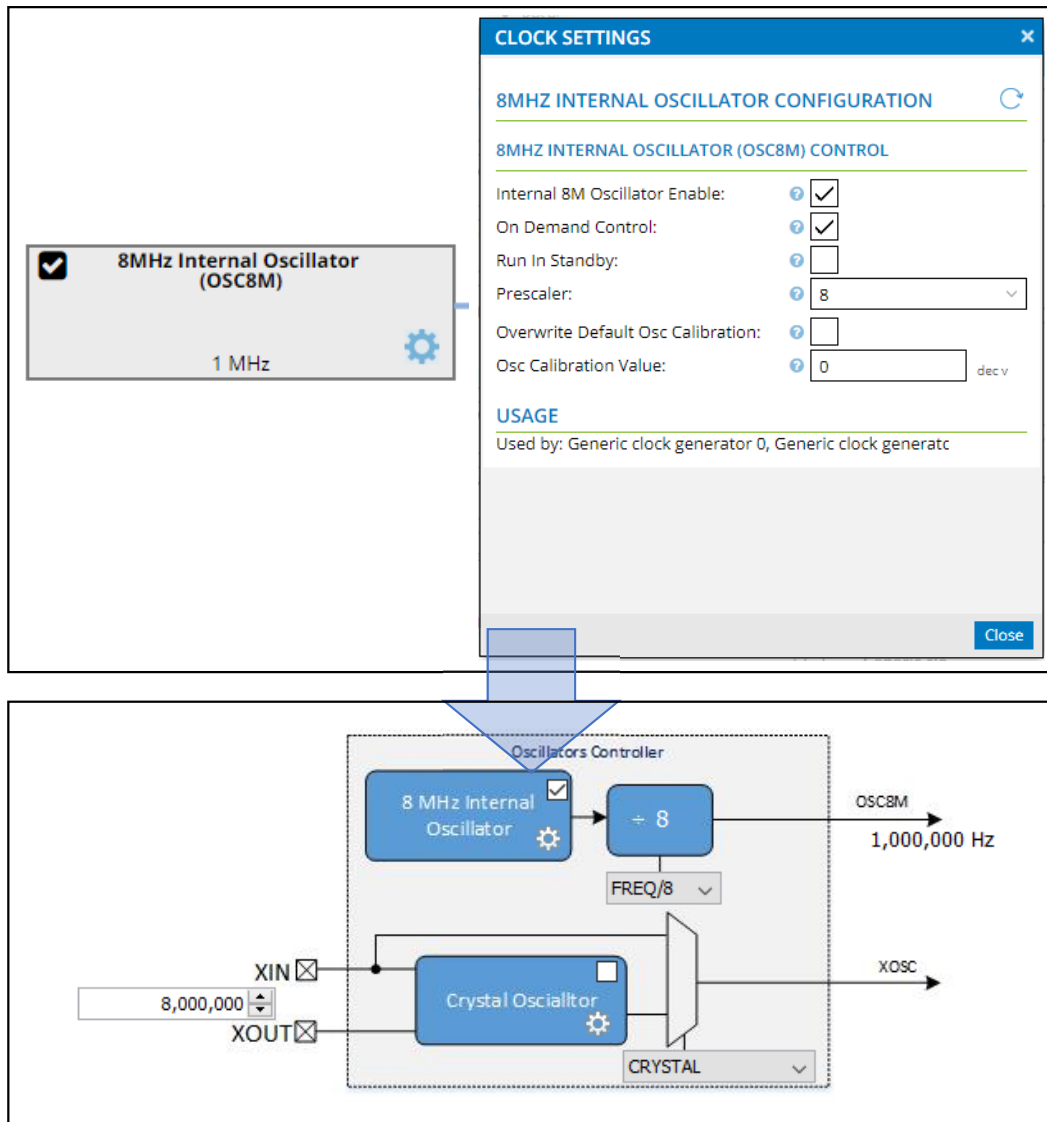
2. To open the project pin configurator interface, in MPLAB X IDE, select *MHC > Tools > Clock Configuration*.

Figure 5-4. Access Clock Configuration Interface Under MHC



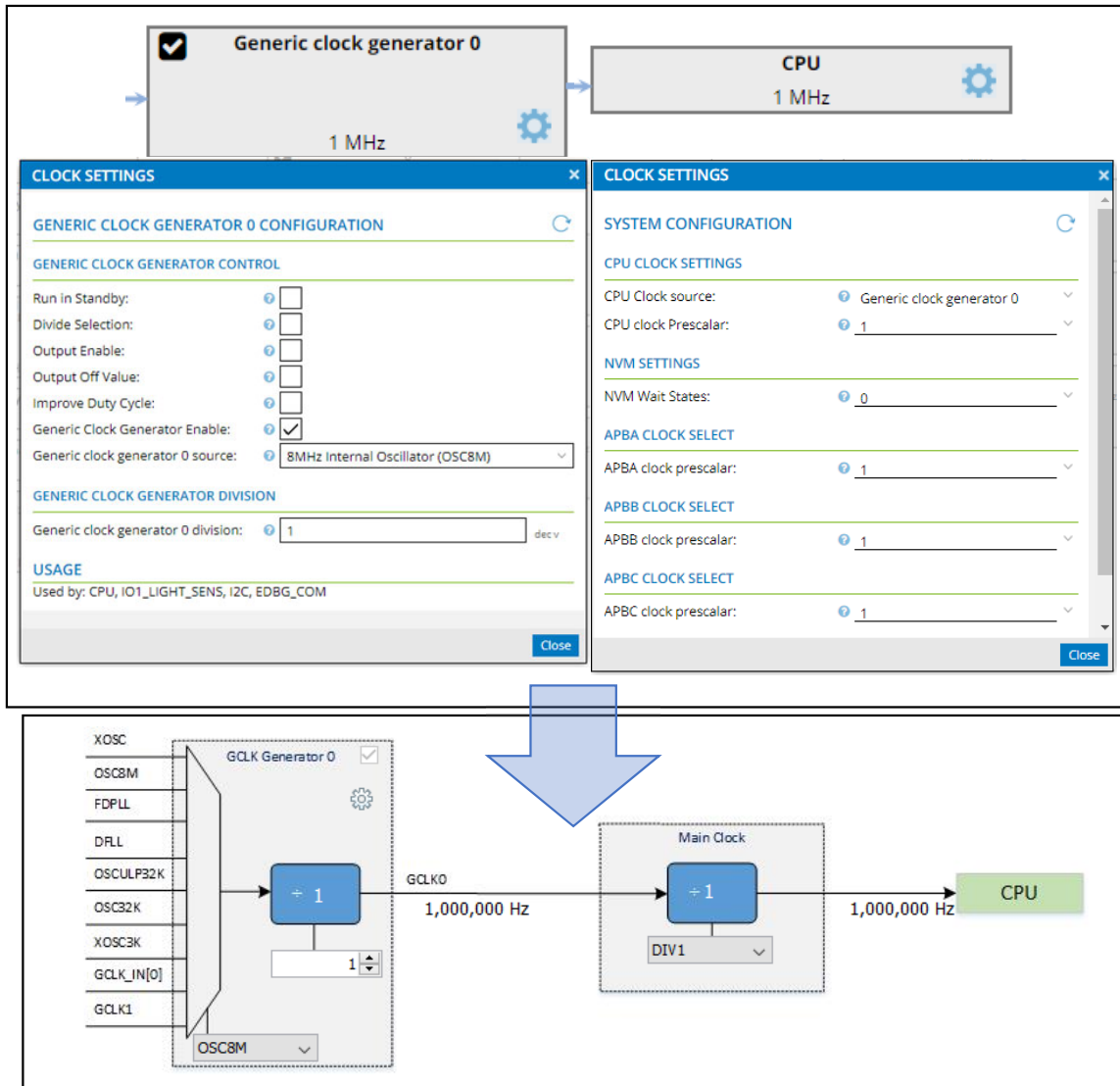
3. Port each clock component configuration from Atmel START to the MHC Clock view.
  - 8 MHz Internal Oscillator (OSC8M)

Figure 5-5. 8MHz Internal Oscillator Configuration Port



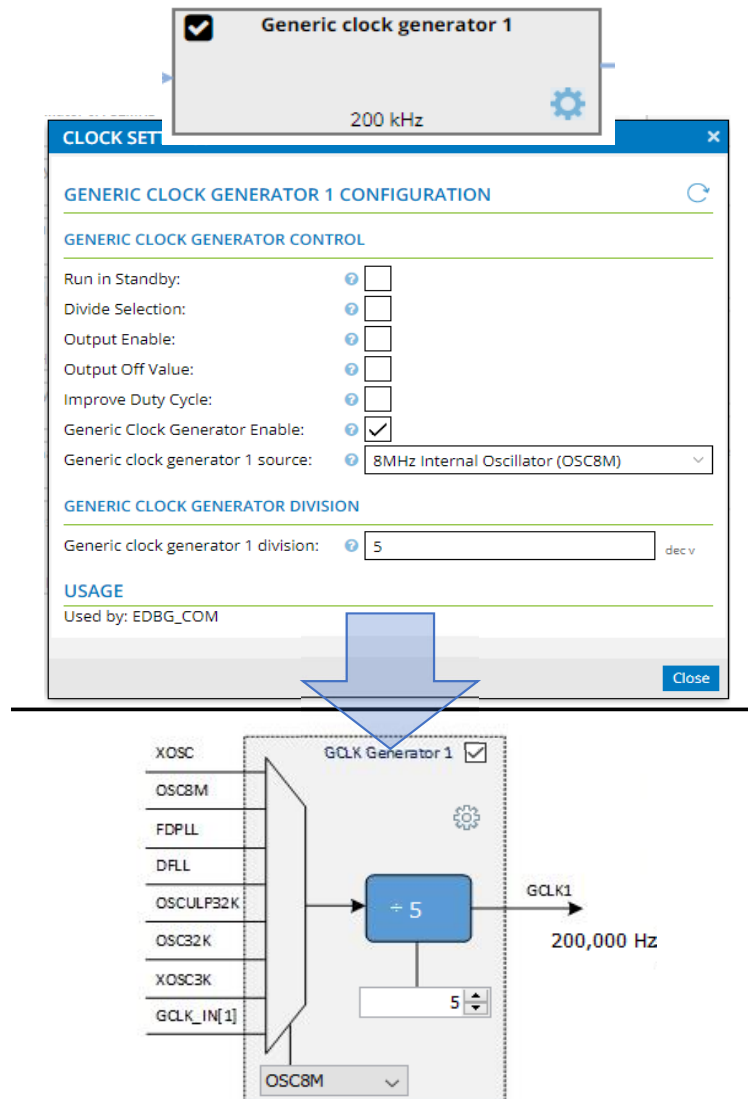
– GCLK Generator 0

Figure 5-6. GCLK Generator 0 Configuration Port



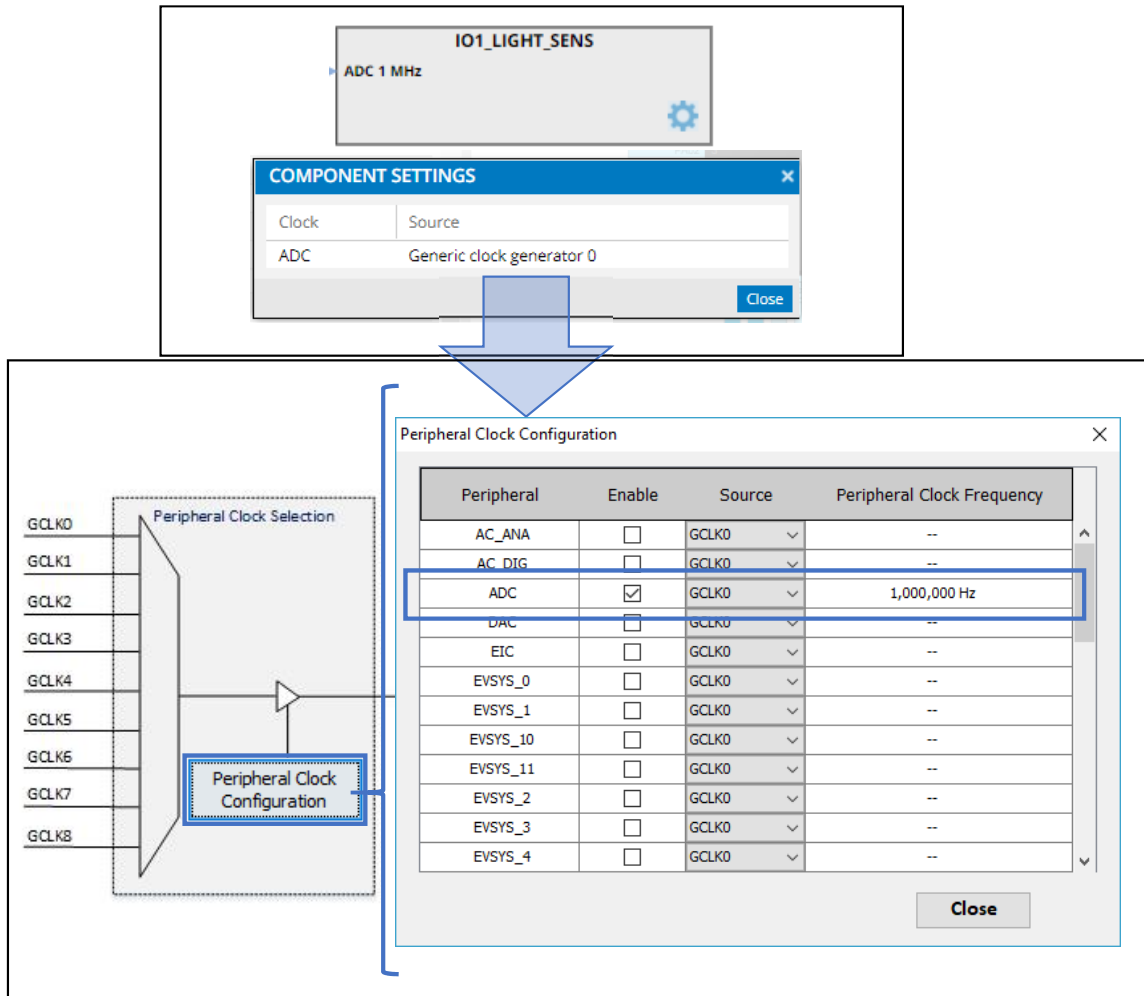
– GCLK Generator 1

Figure 5-7. GCLK Generator 1 Configuration Port



– ADC Peripheral clock

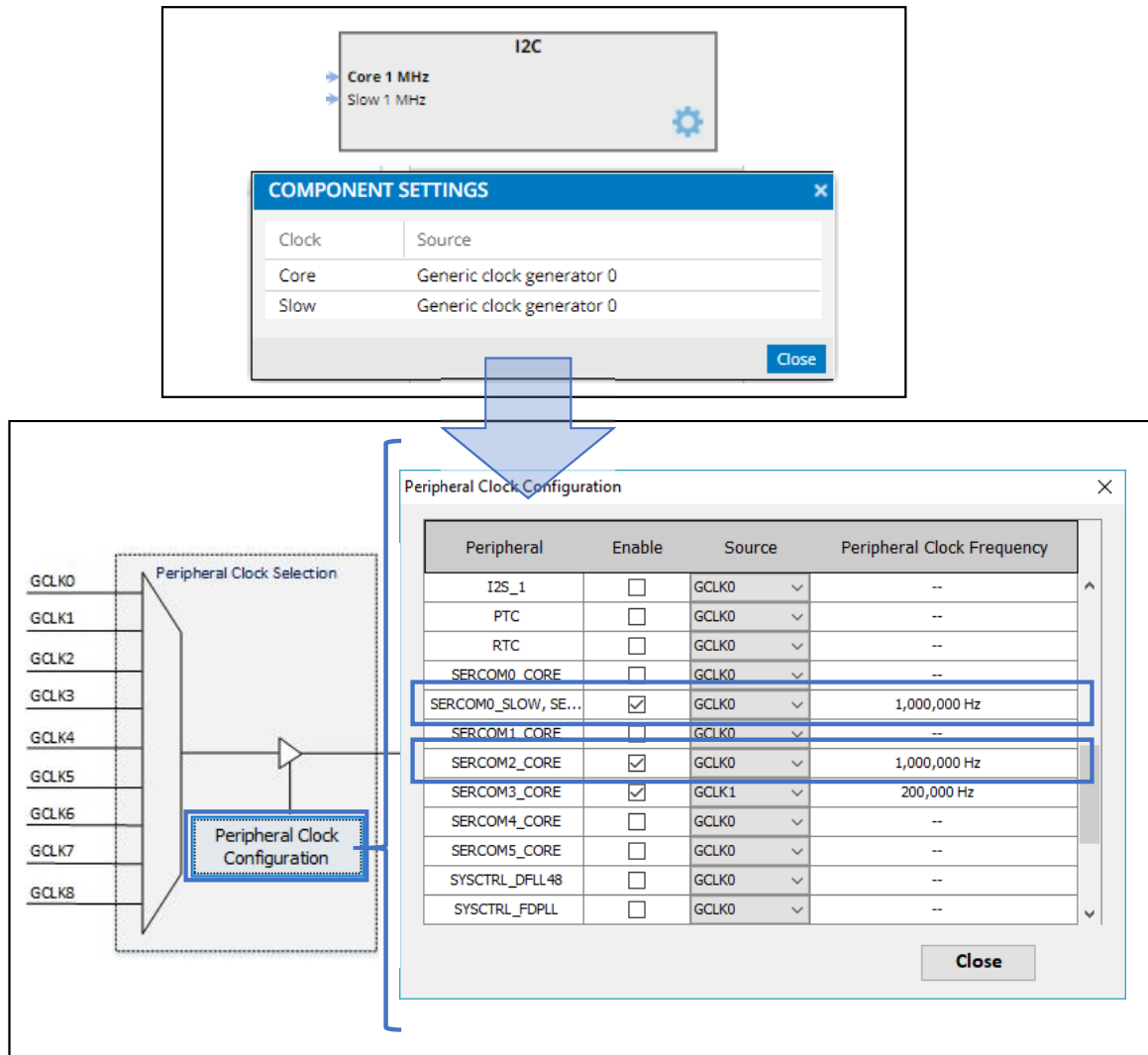
Figure 5-8. ADC Peripheral Clock Configuration Port



– SERCOM2 Peripheral clock

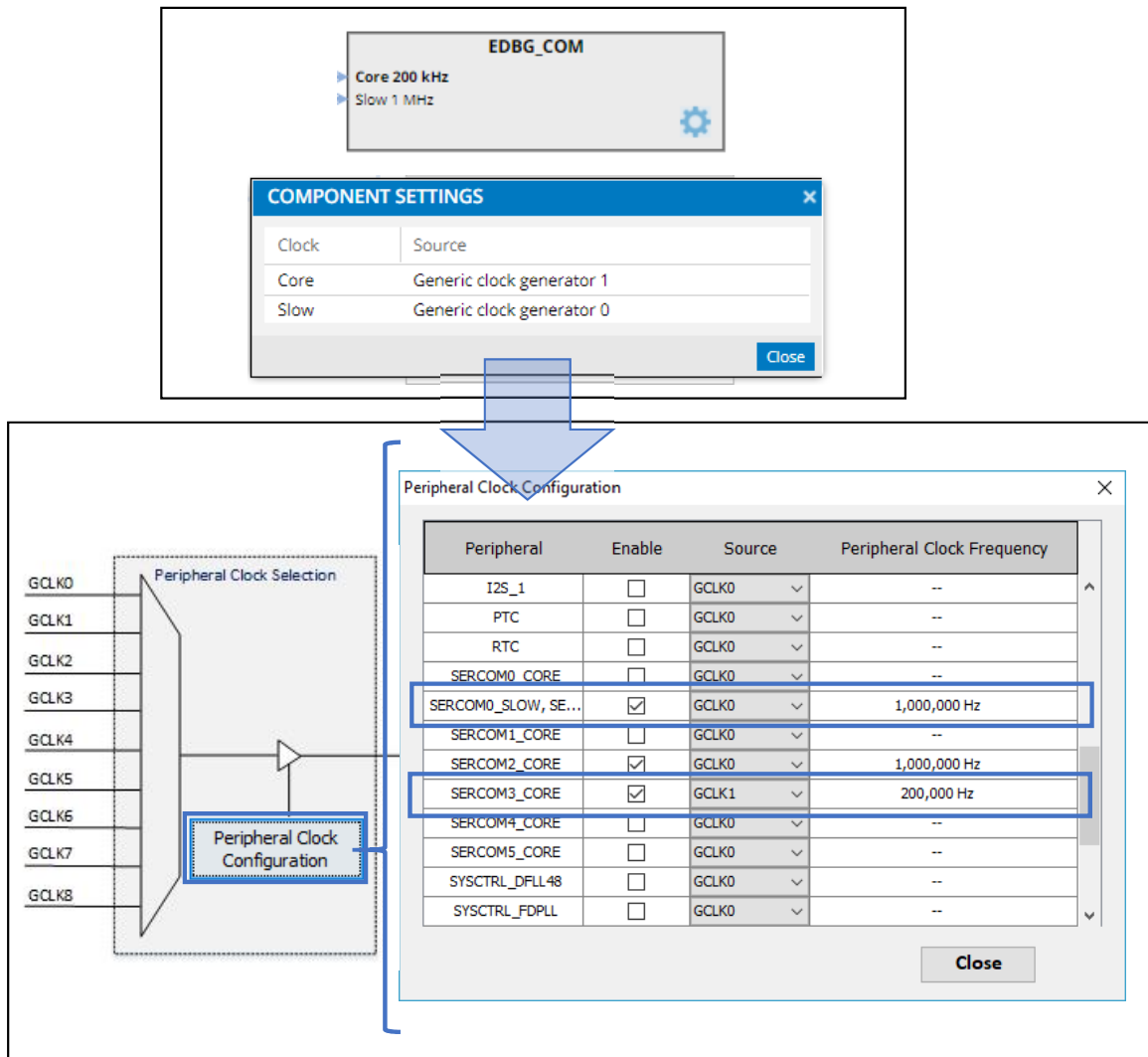


Figure 5-9. SERCOM2 Peripheral Configuration Port



– SERCOM3 Peripheral clocks

Figure 5-10. SERCOM3 Peripheral Configuration Port

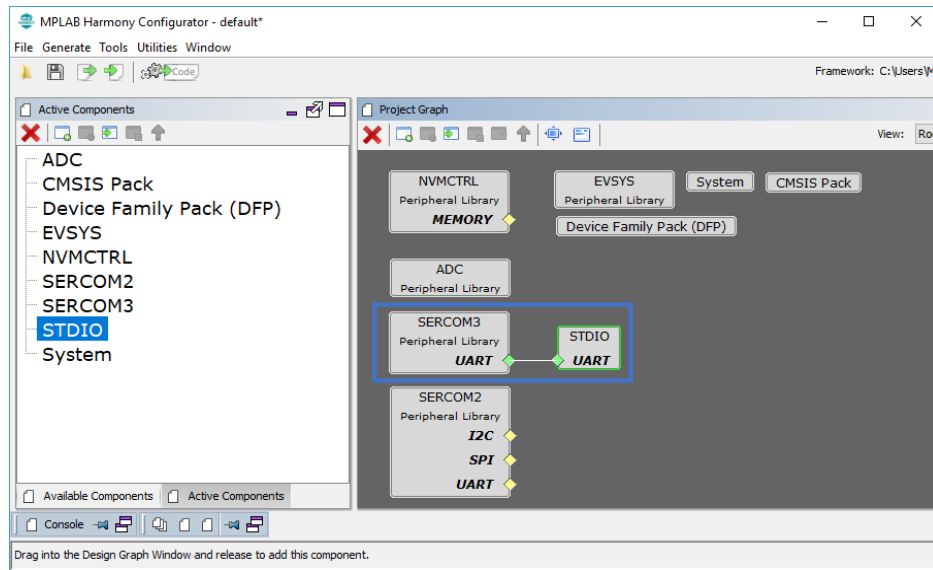


## 6. Porting and Adapting Middleware

The complexity behind the porting of middleware depends on its availability in the MPLAB Harmony 3 framework. The following two approaches can be used for porting middleware:

1. If middleware is available under MPLAB Harmony 3, include the middleware in the MHC project graph and link it to the associated driver. MPLAB Harmony 3 will be automatically generate a Hardware Abstraction Layer customized for this driver and middleware combination.

**Figure 6-1. STUDIO Middleware or Service Integration Under MHC**



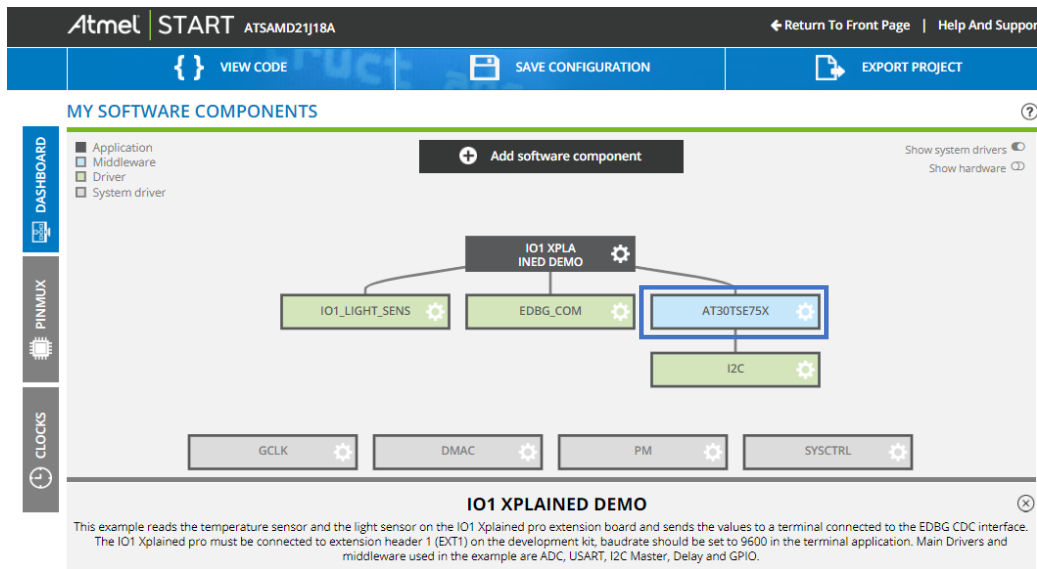
2. If the middleware is not available under Harmony 3, the user must include the source of the middleware in the new project, then locate and modify the Hardware Abstraction Layer to call the APIs provided in the Peripheral Library.

The following steps illustrate the process to be followed for the SAM D21 I/O1 demonstration.

By analyzing the structure of the application (Step 1), using both the Atmel START Dashboard and the application source code, the following middlewares are used in the Atmel START source application:

- AT30TSE75X : temperature sensor
- STDIO redirection (implicitly added)

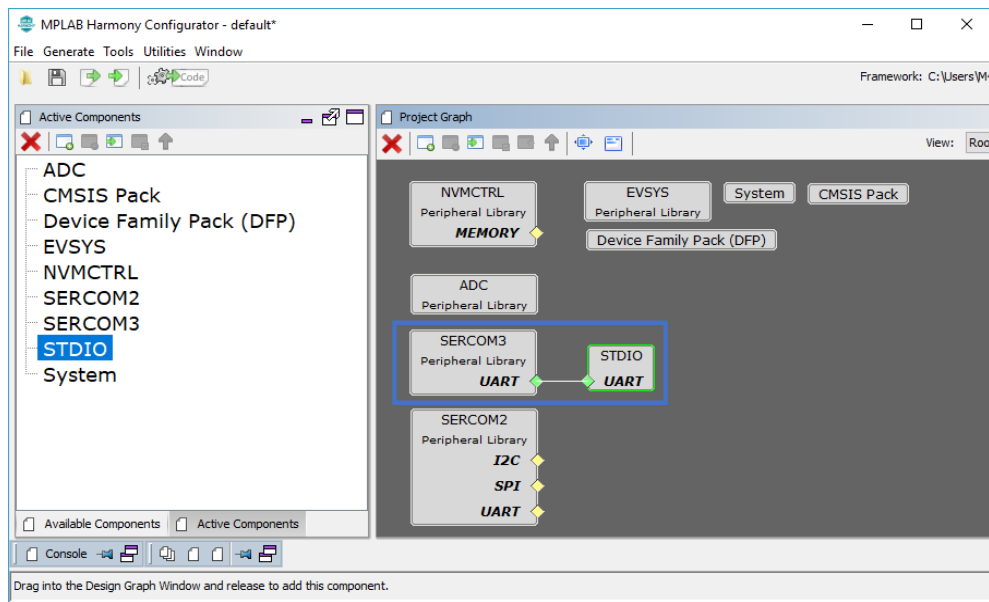
Figure 6-2. Middleware Identification Under Atmel START



The AT30TSE is currently not available under MPLAB X IDE, therefore a manual source port must be created. This step will be covered in step seven when porting the application source code.

1. In MPLAB X IDE, add the STDIO middleware to the project using the MHC Project Graph window then link the STDIO middleware to the SERCOM3 driver.

Figure 6-3. Add STDIO Middleware Under MHC




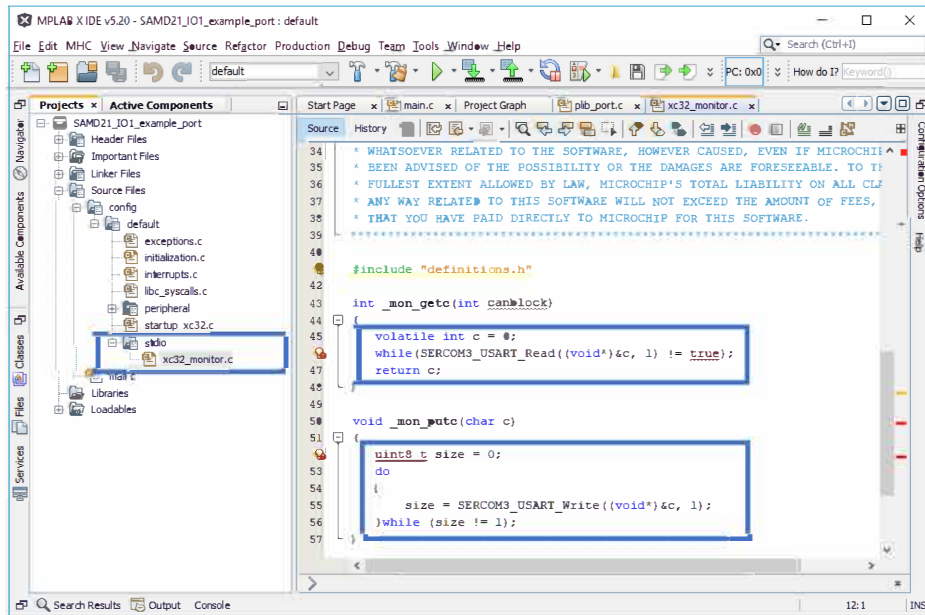
After regenerating the project source from the MHC interface by clicking , the STDIO redirection HAL will be generated according to the graphical configuration. The `getc` and `putc` functions will be redirected to the SERCOM3 APIs.

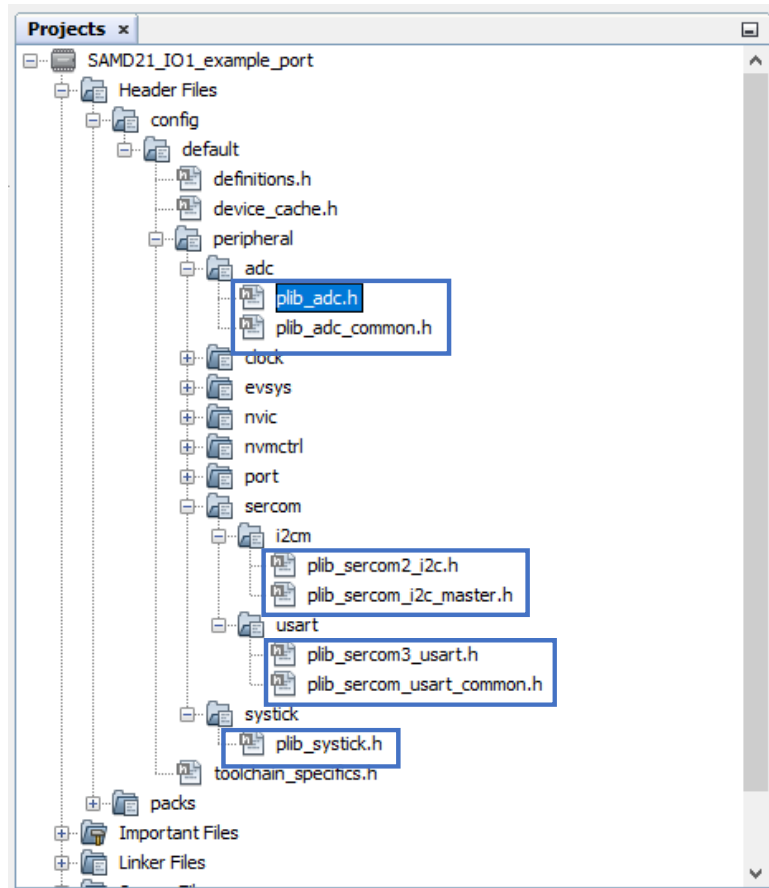
Figure 6-4. MHC Generated STUDIO Redirection



## 7. Porting the Application Source Code

After drivers, system, and middleware layers are ported to the MPLAB Harmony 3 project, all available peripheral libraries and middleware APIs required to port the application are available in the project directory.

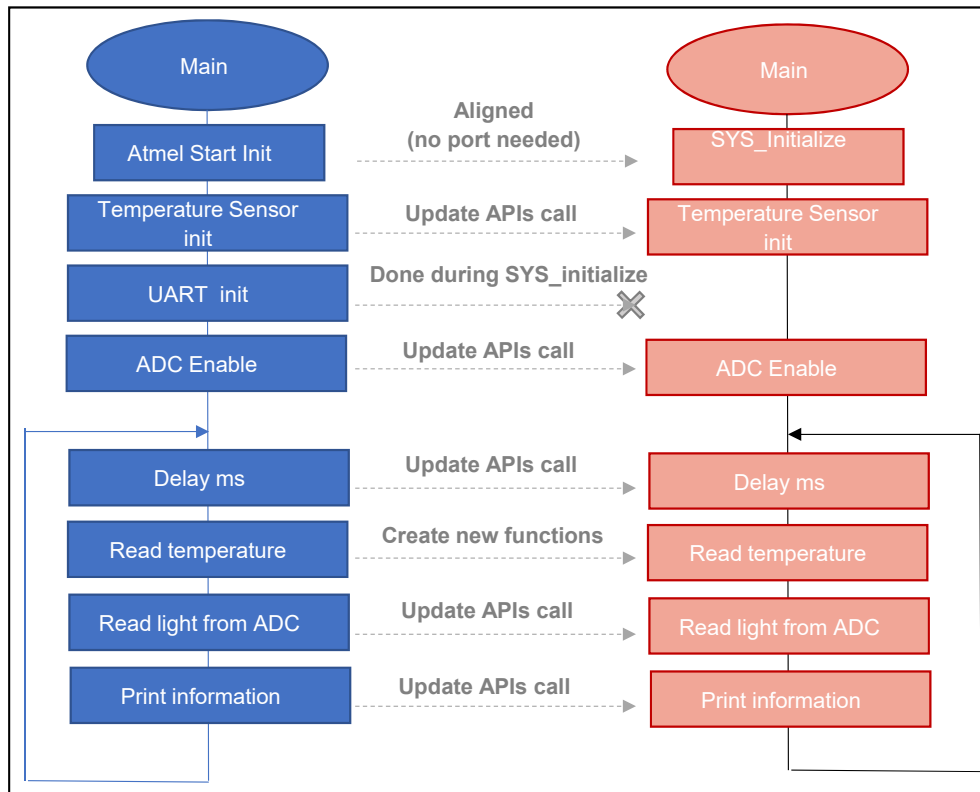
**Figure 7-1. MPLAB Harmony 3 Drivers and Middleware APIs Location**



Porting the application layer requires the review of the Atmel START main application routine and associated application layer related functions, and modifying them to make use of the MPLAB Harmony 3 APIs instead of the Atmel START ones.

The following figure illustrates the effort to port the application layer of the *SAM D21 IO1 Xplained demo* example to MPLAB Harmony 3.

Figure 7-2. SAM D21 I/O1 Xplained Demo Application Layer Porting Effort



The result of the SAM D21 I/O1 Xplained demo application layer porting is shown below :

```
#include <stddef.h>           // Defines NULL
#include <stdbool.h>         // Defines true
#include <stdlib.h>          // Defines EXIT_FAILURE
#include "definitions.h"     // SYS function prototypes

#define AT30TSE_SENSOR_ADDRESS 0x4F
#define STR_SIZE 50

uint16_t adc_read_channel(void);
bool temperature_sensor_init(void);
float temperature_sensor_read(void);

uint16_t adc_read_channel(void)
{
    /*- Start ADC channel */
    ADC_ConversionStart();
    /*- Wait until result is ready */
    while(ADC_ConversionStatusGet() != true);

    return ADC_ConversionResultGet();
}

bool temperature_sensor_init(void)
{
    bool res;
    uint8_t buffer[3];

    /*- Configure the AT30TSE sensor */
    buffer[0] = 1; // Select Configuration register
    buffer[1] = (2 << 5); // Set resolution to 11-bit
    buffer[2] = 0;
    res = SERCOM2_I2C_Write(AT30TSE_SENSOR_ADDRESS,buffer,3);

    return res;
}
```

```
float temperature_sensor_read(void)
{
    uint8_t txbuffer[2];
    uint8_t rxbuffer[2];
    uint16_t data;
    int8_t sign = 1;

    /* - Read the 16-bit temperature register. */
    txbuffer[0] = 0x0; // Select Temperature register
    SERCOM2_I2C_WriteRead(AT30TSE_SENSOR_ADDRESS,txbuffer,1,rxbuffer,2);
    /* - Convert Rx buffer content into temperature */
    data = (rxbuffer[0] << 8) | rxbuffer[1];
    sign -= (bool)(data & (1 << 15)) << 1;
    data &= ~(1 << 15);
    data = data >> (7 - 2);

    return (float)data * sign * (0.5 / (1 << 2));
}

int main ( void )
{
    uint16_t light_val;
    uint16_t temp_result;

    /*- Initialize all modules */
    SYS_Initialize (NULL);
    SYSTICK_TimerStart();
    /*- Enable ADC */
    ADC_Enable();
    /*- Initialize I/O1 temperature sensor */
    temperature_sensor_init();
    while ( true )
    {
        /*- Wait 1 second */
        SYSTICK_DelayMs(1000);
        /*- Read light level */
        light_val = adc_read_channel();
        /*- Read temperature */
        temp_result = temperature_sensor_read();
        /*- Display Light and temperature information on Terminal */
        printf("Temperature: %d Celsius, light sensor: 0x%x\r\n",
            temp_result, light_val);
    }
    /*- Execution should not come here during normal operation */
    return ( EXIT_FAILURE );
}
```



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