BIOS Version 1.2.11 for Dell EMC PowerEdge Servers: R740, R740XD, R640, and R940.

And for Dell Precision 7920 Rack workstation

**Release Notes** 



### **Release Notes**

#### BIOS

Basic Input / Output System (BIOS) facilitate the hardware initialization process and transition control to the operating system.

#### **Current Version**

1.2.11

#### **Release Date**

October 2017

#### **Previous Version**

1.1.7

## Importance

**URGENT**: Dell EMC highly recommends applying this update as soon as possible. The update contains changes to improve the reliability and availability of your Dell system.

## Platform(s) affected

The PowerEdge R740, R740XD, R640, and R940 servers. Also, for the Precision 7920 Rack workstation.

### What's New

- Added support for updating the NVDIMM-N firmware version.
- Updated the Intel Trusted Execution Technology (Intel TXT) BIOS Authenticated Code Module (ACM) to version 1.3.3.
- Updated the Intel Processor and Memory Reference Code to PLR2.
- Updated the Intel Xeon Processor Scalable Family Processor Microcode to version 0x2C.
- In the Performance System Profile, the default attribute of C-States is changed to Disabled.

# Fixes

- Mitigated the security vulnerability CVE-2017-5706 (<u>http://www.cve.mitre.org/cgi-bin/cvename.cgi?name=2017-5706</u>) details anticipated to be published November 20, 2017.
- Mitigated the security vulnerability CVE-2017-5709 (<u>http://www.cve.mitre.org/cgi-bin/cvename.cgi?name=2017-5709</u>) details anticipated to be published November 20, 2017.

### **Important notes**

This release provides continued code optimization to improve stability and performance.

### **History of previous Release Notes**

Version

1.1.7

#### **Release Date**

August 2017

#### What's New

- Added a message to indicate that the PCIe link is broken and unable to train.
- Enabled the PCIe ECRC (End to End Cyclic redundancy check) error reporting
- Added NVDIMM-N improvements for Address Range Scrubbing.
- Updated the Intel Trusted Execution Technology (Intel TXT) BIOS and SINIT Authenticated Code Module (ACM) to version 1.3.2.
- Updated the Intel Processor and Memory Reference Code to PLR1.
- Updated the Intel Xeon Processor Scalable Family Processor Microcode to version 0x2B.
- Added the POST-stage error message when installed DIMMs are mapped out.

#### Fixes

- A CPU core is not available when the 8-core CPUs are used.
- Persistent NIC naming is incorrect when certain NDC is installed.

#### Version

1.0.7

#### **Release Date**

July 2017

#### What's New

Updated the ProcessorM to version 0x26.

### Fixes

None for this release.

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